NADIMPALLI SATYANARAYANA RAJU INSTITUTE OF TECHNOLOGY (AUTONOMOUS) INTEL STATE OF TECHNOLOGY NSRIT

ADAR 2021-2822

3.3.2 - Number of workshops/senitors essenceed on Research Methodology, Intellectual Property Rights (PR), Dryrepresearchip and Skill Development during the year

89	Nume of the Werkshop/Seminer	Number of Participants	Oata (From - To)	Link to the Workshop/Seminar report on the wobsite
4	Wonaires or M. & D. using sylfest	99	29-04-2022 @ 29-04-2022	
	Especticals " Overview of Televient networks & GSM broad band, Mulliphy FFTH"	141	09-19-2021 to 09-10-2021	
	Description on "Townshormation of Communication Techniques from sid ans in new ent"	132	12-19-2921 # 12-10-2021	
-	CHUS, Press -1 Descriptions catally	64	10-13-21 to 22.01.22	
-	CEUS - Prava-2 Criteria e viente	66	15-03-22 to 28-05-22	
1	SOLAIS Bases	139	18-10-21 to 30.01.22	
7	GOT, East Poten Proputation	120	28-03-22 to 14-06-23	
	CALIFIERY, 2021	90	18-11-2021	
	(CDP) Server 4. Burling of many publicity	62	18-10-21 @ 22.01.20	
10	/Dis Chan 2 Interior miles	38	28-02-22 to 04-06-21	
14	NV BT	34	04-03-22 to 13-05-22	
10	Samer on Art of Weiver Technical Paper	32	06-08-22	
10	Cause call Thinking	192	23-02-3022	
14	Colores March 2022	116	12-63.3022	
15	Sili inigral Hackbox 202	54	28-03-2102 to 29-03-2022	
14	STay's West Store man Tealware Programme int WVS	120	30-06-3022 to 02-07-2022	
11	Testmotogical Adversaments in Move. Defense and Space Applications. An integrated Industry 50 Approach	88	08-07-2023 30 10-07-2023	
18	Addave Manufacturing of Caranic nuterials	50	08-06-2021	
15	Flede Control Actuators	40	19-08-2021	
-26	CEMS- Phone -1-Esiantials for NK Designory	101	23-10-2021 to 18-12-2021	
21	CENS- Praw 3 Vertabulos	54	05-03-2022 to 04-14-2022	
- 20	Appled Ritholic Central Lab	33	01-11-1101 to 31-03-2022	
23	Consular Aland Nodelins SOC-1	47	18-10-2021 to 05-02-2822	
24	Computer Numerical Cantor Programming SDC-1	47	28-02-802210-84-06-2822	
25	Outcome Beesel Education and Revised Block's Texnology , it is proposed to conduct a Tex-week PDP an OBAL Decome Based Accreditation) among the termar of Centra of Academic Excellence (A Guarty Interfve. of IGAC)	12	254623221646473322	
28	Computer Aces Building Drawing (80C -1)	33	18-30-2021 to 29-01-2022	
37	Builded Internation Modeling Life (SOC-II)	33	14-12-2022 to 28-05-2022	

Dr. J. Raja Murugadoss Director N.S. Raju Institute of Technology (A) Sontyam, Visakhapatnam-531173





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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON ONE DAY WORKSHOP ORGANISED BY ECE DEPARTMENT

The Department of Electronics and Communication Engineering organised a one-day workshop "ML & DL using Python" organised on 29-04-2022 in ECE seminar hall. It was organized with the prior permission from Honourable Principal Dr. M A Khadar Baba.

RESOURCE PERSON: Dr. E. Kiran Kumar, Associate Professor, K L University

The session started with a welcome Speech by Mr. Y. Sravana Kumar, Assistant Professor, Dept. of ECE, in the presence of Dr. B. Siva Prasad, HOD, Dept. of ECE. The event has been coordinated by Mr. Y. Sravana Kumar and Mr. K. Rajasekhar.

The lecture was focused on the key concepts related to machine learning, data learning and the participants received a brief overview of the following topics: Introduction to Python, Python Installation, Python Basics and Native Types, Functional Programming with Python, Introduction to Machine Learning, Future of Machine Learning in AI, Installing Anaconda and Jupyter, Overview of Numpy and Pandas, Understanding Machine Learning Algorithms, Introduction to Object Detection, Building a Face Recognition App. The interesting examples from around the world were the highlight of the session. Workshop was more informative.

The session turned out to be interactive and ended on a high note, with 90 students participated actively in the session and some of the students reflecting on their thoughts on how to implement their ideas using IOT to make world a better place.

Signature of HoD

N.S. Raju Institute of Technology Sontyam, Visakbapathern-531, 171

















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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON EXPERT TALK ORGANISED BY NSRIT IETE STUDENT CHAPTER

The Department of Electronics and Communication Engineering organised an expert talk through the departmental IETE Student Chapter on the topic 'OVERVIEW OF TELECOM NETWORKS & GSM, BROAD BAND, MULTIPLY, FTTH', on 09-10-2021 in ECE seminar hall for a duration of 3 hours. It was organized with the prior permission from Honourable Principal Dr. M A Khadar Baba.

OBJECTIVES:

- Advances in day-to-day communication
- Latest applied technologies
- > Opportunities for future and higher education

RESOURCE PERSON:

1. Mr.S. Suresh Kumar , Board of Directors, Director - (Finance), BSNL.

As per the instructions of Dr. B. Siva Prasad, HoD ECE dept., the programme started at 9:30 on 09-10-2021 in the ECE seminar hall with a welcome not by Mr. P.V.J. Raj Kumar, Asst. Professor (ECE).

The session was all about creating interest to learn and be familiar with latest technology by guiding the students and sharing the advanced knowledge and latest technologies which are being applied and are in working in day-to-day communication technology. A brief description has been provided by the speaker on GSM, Broad Band technologies and FTTH (Fiber To The Home). Significance of Optical Communication in modern day has been explained.

Finally in the Q&A session, there were questions raised about the feasibility of wireless optical communication for long distance.

The session turned out to be interactive and ended on a high note, 141 students participated actively in the session.

Signature of HoD Head of the Department Dept of Electronics & Communication Engo. N.S.Rajar Institute of Technology Sontyam, Visakhapat nam-534 173













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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON GUEST LECTURE ORGANISED BY NSRIT IETE STUDENT CHAPTER

The Department of Electronics and Communication Engineering organised a guest lecture through the departmental IETE Student Chapter on the topic "TRANSFORMATION OF COMMUNICATION TECHNIQUES FROM OLD ERA TO NEW ERA", on 12-10-2021 in ECE seminar hall for a duration of 2 hours. It was organized with the prior permission from Honourable Principal Dr. M A Khadar Baba.

OBJECTIVES :

- Introduction to Communication
- Types of Communication
- Evolution in the Communication Methods
- Communications Historical Time Line
- Advantages and disadvantages of high frequency usage in modern day communication

RESOURCE PERSON: Dr. P. Mallkarjuna Rao, Professor, Andhra University, Vizag.

As per the planned schedule, the programme has been started at 11:30am on 12-10-2021 in the ECE seminar hall with a welcome note by Dr. B. Siva Prasad, Assoc. Professor, HoD (ECE).

The session started with the introduction to Communication and brief idea has been provided to students about different communication types, techniques and changes occurred in the communication from the beginning.

Finally in the Q&A session, there were questions raised by the students about the possibility of 6G in India and other advanced communication methods

The session turned out to be interactive and ended on a high note with 132 students actively participating in the session.

Signature of HoD

Bept of Eactonics& Communication Engo. N.S.Raju Institute of Technology Sontyam, Visaknapetnam-531 173



NSRIT

TRANSFORMATION OF COMMUNICATION TECHNIQUES FROM OLD ERA TO NEW ERA

PROF.P. NALLIKARJUNA RAD Professor, Andrea University, Vizag

TUESDAY, October 12, 2021, III 30 AM

AUTONEMOUS

















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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERINGONE CREDIT COURSE "DESIGN OF BASIC ELECTRONIC CIRCUITS"

II B.TECH - I SEM A.Y. 2021-22(ODD SEMESTER)

S.No	Regd.No	Name of the Student	Course Name
1	20NU1A0401	ADAPA GOWRI PRASAD	Design of basic electronic circuits
2	20NU1A0402	ADURTHY VENKATA SAI HARSHITH	Design of basic electronic circuits
3	20NU1A0403	AKKIREDDI ROHITHRAJ	Design of basic electronic circuits
4	20NU1A0404	ALETI V M R BHARATH KUMAR	Design of basic electronic circuits
5	20NU1A0407	ANNAMREDDY PURNA SAI SRI RAM	Design of basic electronic circuits
6	20NU1A0408	ARLI SRAVYA	Design of basic electronic circuits
7	20NU1A0409	ARON ABRAHAM	Design of basic electronic circuits
8	20NU1A0413	BAIREDDY ANUSHA REDDY	Design of basic electronic circuits
9	20NU1A0415	BEESETTI VARSHIT	Design of basic electronic circuits
10	20NU1A0418	BOKKA BHAVANA	Design of basic electronic circuits
11	20NU1A0419	BOOSA ROHINI	Design of basic electronic circuits
12	20NU1A0420	BORRA VENKATA MOUNIKA	Design of basic electronic circuits
13	20NU1A0421	BOYI JANAKI RAM	Design of basic electronic circuits
14	14 20NU1A0422 BYLAPUDI HEMA SIVA SAI MADHURI		Design of basic electronic circuits
15	20NU1A0423	CHALLA KALYAN KUMAR	Design of basic electronic circuits
16	20NU1A0424	CHATLA GEETHANJALI	Design of basic electronic circuits
17	20NU1A0425	CHEEPURUPALLI VEDAVYAS	Design of basic electronic circuits
18	20NU1A0427	CHINTALA MUTYALA VENKATA SATYA MURTHY	Design of basic electronic circuits
19	19 20NU1A0429 DEGALA DURGA PRASAD		Design of basic electronic circuits
20	20 20NU1A0431 GADI POORNA JHANSI		Design of basic electronic circuits
21	20NU1A0432	GADI RAMARAJYALAKSHMI	Design of basic electronic circuits
22	20NU1A0433	GANDHAM USHA SURYAVATHI	Design of basic electronic circuits
23	20NU1A0435	GAVARA TULASI	Design of basic electronic circuits
24	20NU1A0438	GUBBALA GNANENDRA KUMAR	Design of basic electronic circuits
25	20NU1A0439	GUDE SRAVANI	Design of basic electronic circuits
26	20NU1A0440	GULIPILLI KARISHMA RANI	Design of basic electronic circuits
27	20NU1A0444	KADIYAM ANUSHA	Design of basic electronic circuits
28	20NU1A0445	KALIVARAPU LOKESH	Design of basic electronic circuits
29	29 20NU1A0449 KANTUBOTHU VASAVI		Design of basic electronic circuits
30	20NU1A0452	KODAMATI GNANESWARA RAO	Design of basic electronic circuits
31	31 20NU1A0455 K KALAYANI		Design of basic electronic circuits
32	20NU1A0458	KOTHAKOTA DIVYA SRI RAMANI	Design of basic electronic circuits
33	33 20NU1A0459 KOTHAKOTA JANAKI		Design of basic electronic circuits
34	20NU1A0460	KOTYADA VIJAYA LAKSHMI	Design of basic electronic circuits

S.No Regd.No		Name of the Student	Course Name
35	20NU1A0464	LENKA RENUKA SOWJANYA	Design of basic electronic circuits
36	20NU1A0465	MAJII NAMRATHA	Design of basic electronic circuits
37	20NU1A0468	MENTI TEJA	Design of basic electronic circuits
38	20NU1A0472	MULAGAPAKA DHARANI SREE	Design of basic electronic circuits
39	20NUIA0473	MANUKONDA AVINASII	Design of basic electronic circuits
40	20NU1A0474	MURALIDHAR ALLU	Design of basic electronic circuits
41	20NU1A0475	NRAHIMKUMAR	Design of basic electronic circuits
42	20NU1A0476	NODAGALA YARRAJEE SAI KARTHIK	Design of basic electronic circuits
43	20NU1A0477	NAGARAJU RAVINDRA VARMA	Design of basic electronic circuits
44	20NU1A0479	NEKKELLA RAMYA	Design of basic electronic circuits
45	20NU1A0482	NARENDRUNI SANDEEP	Design of basic electronic circuits
46	20NU1A0484	NIMMAKAYALA VENKATA SRI KALA PRANEETHA	Design of basic electronic circuits
47	20NU1A0485	PACHIGULLA CHETAN VIKAS	Design of basic electronic circuits
48	20NU1A0486	PADAMATA YAKSHA SAI	Design of basic electronic circuits
49	20NU1A0488	PALLA GAYATHRI	Design of basic electronic circuits
50	20NU1A0491	PATCHIPULUSU SRAVANI	Design of basic electronic circuits
51	20NU1A0492	PILLA SRAVANI	Design of basic electronic circuits
52	20NU1A0493	P ROHIT REDDY	Design of basic electronic circuits
53	20NU1A0494	POTNURU SRIVALLI	Design of basic electronic circuits
54	20NU1A0498	REDDI NAVYA JYOTHI	Design of basic electronic circuits
55	20NU1A04A1	RONGALI SAILAVANYA	Design of basic electronic circuits
56	20NU1A04A4	SAVITHINI DURGA VENKATA MOHAN	Design of basic electronic circuits
57	20NU1A04B0	SWARANGI TANUJA	Design of basic electronic circuits
58	20NU1A04B2	TAMMINENI KOMALI	Design of basic electronic circuits
59	20NU1A04B3	TELU HOMESH	Design of basic electronic circuits
60	20NU1A04B4	THOTAKURA VIMALA SAI SAROJINI	Design of basic electronic circuits
61	20NU1A04B5	TONANGI ROSHITHA	Design of basic electronic circuits
62	20NU1A04B6	UPPILI CHANDRA MOULI Decise of basic electron	
63	20NU1A04B9	YELAMANCHI PUJITHA	
64	20NU1A04C0	YELLAPU NEERAJ KUMAR	Design of basic electronic circuits

c. 90 Faculty co-ordinator

1110 Signature of HOD

Figure of the Department Sign of Electronic & Communication English N.S. Raju Institute of Technology Sontyom, Visakhapatham-531 173

CEMS SIEMENS RCLASS AGARMA Centre of Excellence in Maritime and Shipbuilding (CEMS) (A 1941 Development Initiative by Gost, of India / Ministry of Mipping / Jogarnalis) This is to certify that Mr /Ms. Pilla Sravani studying B. Tech III. Semester of ECE Department having REGD No. 20NUIA 0492 From Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag Has successfully completed One Credit Course with industry Oriented Training on Design of Basic Electronic Circuits Conducted by Centre of Excellence in Maritime and Shipbuilding (CEMS), Gandhigram, Visakhapatnam From 23/10/2021 10 18/12/2021 Authorized Signatory AP/ VIZ/ 4002 / 11-2021/0049 Certificate No. 18/ 12/ 2021 Date: Cdr.Goptkrishna Sivvan, PV (Retd.) Chief Operating Officer-CEMS.

NAMES OF BRIDES, OFF

CEMS SIEMENS 1201 455 SAGARMALA Centre of Excellence in Maritime and Shipbuilding (CEMS) (A Shifl Development initiative by Cook, of Iraks / Ministry of Suppley / Supervisite) This is to certify that Mr /Ms P. Rohit Reddy studying B. Tech W. Semester of ECE Department having REGD No 20 NOIA 0493 From Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag Has successfully completed One Credit Course with Industry Oriented Training on of Basic Electronic Circuits Design Conducted by Centre of Excellence in Maritime and Shipbuilding (CEMS), Gandhigram, Visabhapatnam From 23 / 10/2021 to 18 / 12 / 2021 Authorized Signatory AP/ VIZ / 4002/ 11-2021/0033 Certificate No. 18/12/ 2021 Date: Cdr.Gopikrishna Savana IN (Retd.) Chief Operating Officer CEMS

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ONE CREDIT COURSE "EMBEDDED SYSTEMS"

II B.TECH -II SEM A.Y. 2021-22 (EVEN SEMESTER)

Registered Students Data (Every Saturday)

S.No	Regd.No	Name of the Student	Course Name	
1	20NU1A0401	ADAPA GOWRI PRASAD	Embedded Systems	
2	20NUI A0404	ALETI V M R BHARATH KUMAR	Embedded Systems	
3	20NU1A0405	A MADHURI	Embedded Systems	- 4
4	20NULA0409 ARON ABRAHAM		Embedded Systems	
5	20111140413	BAIREDDY ANUSHA REDDY	Embedded Systems	
6	20110140413	BODALA PAVANKIMAR	Embedded Systems	
7	20110140417	BOOSA ROHINI	Embedded Systems	1
	20140120419	BODDA VENKATA MOUNIKA	Embedded Systems	
0	20101740420	BOVI JANAKI BAM	Embedded Systems	
10	20NUIA0421	BVI ADUDI HEMA SIVA SAI MADHURI	Embedded Systems	
10	2010140422	CH VALVAN KUMAR	Embedded Systems	
12	2010170423	CHEEDURIDALLI VEDAVVAS	Embedded Systems	
12	20NUTA0425	CH VENKATA SATVA MURTHY	Embedded Systems	1
13	20NUTA0427	CH VENKATA SATTA MORITI	Embedded Systems	
14	20NU1A0429	CADI DOODNA IVANEL	Embedded Systems	
15	20NU1A0431	GADI POOKNA JHANSI	Embedded Systems	1
16	20NUIA0432	GADI KAMAKAJ YALAKSHMI	Embedded Systems	
17	20NULA0435	GAVARA TULASI	Embedded Systems	
10	201101/10439	GUDE SRAVANI	Embedded Systems	
20	201101740439	GULIPILLI KARISHMA RANI	Embedded Systems	
20	20110120444	KADIVAM ANUSHA	Embedded Systems	
21	2010120446	KLOKESH	Embedded Systems	
22	2000140440	K IASWANTH	Embedded Systems	
23	20N01A0447	KODAMATI CNANESWARA RAO	Embedded Systems	
24	20NUTA0452	KODAMATI ONANCOWARA RAO	Embedded Systems	-
25	20NU1A0455	K KALAYANI	Embedded Systems	-
26	20NU1A0458	KOTHAKOTA DIVYA SRI RAMANI	Embedded Systems	-
27	20NU1A0460	KOTYADA VIJAYA LAKSHMI	Embedded Systems	
28	20NU1A0463	K RITHIKA PATNAIK	Embedded Systems	
29	20NU1A0464	LENKA RENUKA SOWJANYA	Embedded Systems	
30	20NU1A0472	MULAGAPAKA DHARANI SREE	Embedded Systems	

31	20NU1A0473	MANUKONDA AVINASH	Embedded Systems
32	20NULA0475	N RAHIMKUMAR	Embedded Systems
33	20NULA0476	NODAGALA YARRAJEE SAI KARTHIK	Embedded Systems
34	20NU1A0479	NEKKELLA RAMYA	Embedded Systems
35	20NU1A0482	NARENDRUNI SANDEEP	Embedded Systems
36	20NU1A0484	N V S K PRANEETHA	Embedded Systems
37	20011100488	PALLA GAVATRI DEVI	Embedded Systems
38	20NU1A0489	PANDA DEEPTHI	Embedded Systems
10	20NU1A0492	PILLA SRAVANI	Embedded Systems
40	20NU1A0494	POTNURU SRIVALU	Embedded Systems
41	20NUIA0496	RHARIKA	Embedded Systems
42	20NULA0498	REDDI NAVYA IVOTHI	Embedded Systems
43	20NULA0499	R NIHARIKA LAKSHMI	Embedded Systems
44	20NULA04A1	R SALLAVANYA	Embedded Systems
45	20NU1A04B5	TONANGI ROSHITHA	Embedded Systems
46	20NU1A04C0	YELLAPU NEERAJ KUMAR	Embedded Systems
47	21NU5A0401	A TEJASWI	Embedded Systems
48	21NU5A0402	A SAI SURYA KIRAN	Embedded Systems
49	21NU5A0403	B LASKHMI NAIDU	Embedded Systems
50	21NU5A0404	B YAMINI	Embedded Systems
51	21NU5A0405	B LOKANANDHINI	Embedded Systems
52	21NU5A0406	CH S L S UMA BHANU	Embedded Systems
53	21NU5A0407	G BHOOMIKA	Embedded Systems
54	21NU5A0408	J THANMAYEE SUDHA	Embedded Systems
55	21NU5A0409	K SRAVANA	Embedded Systems
56	21NU5A0410	K TANUJA RANI	Embedded Systems
57	21NU5A0411	P HIMA BINDHU	Embedded Systems
58	21NU5A0412	PUTTA ROHINI	Embedded Systems
59	21NU5A0413	S JANAKI RAM LAKSHMI	Embedded Systems
60	21NU5A0414	S KASI VISWANADHAM	Embedded Systems
61	21NU5A0415	S PADMINI RANI	Embedded Systems
62	21NU5A0416	S PREM KUMAR	Embedded Systems
63	21NU5A0417	VEMADAPADU SAI NAVEEN	Embedded Systems
64	21NU5A0418	Y CHANDRA KANTH	Embedded Systems
65	21NU5A0419	Y DEEPIKA PRIYA	Embedded Systems

SKSU R

Faculty Coordinator

Signature of HOD

Head of the Department Dept of Electronics & Communication Engs, N.S. Raju Institute of Technology Sontyam, Visakhapatham-531 173



This is to certify that Mr. /Ms. LENKA RENUKA SOWJANYA Studying

B.Tech II Year of ECE Department having Roll No 20NU1A0464 from

Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag has successfully

Completed One Credit Course with Industry Oriented Training on Embedded Systems

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Visakhapatnam

From 5th March 2022 to 4th June 2022

Place: Visakhapatnam

Date: 16th June 2022



Authorized Signatory

Cdr. Gopikrishna Sivvam, IN(Retd.) Chief Operating Officer-CEMS



This is to certify that Mr. /Ms. MULAGAPAKA DHARANI SREE Studying

B.Tech II Year of ECE Department having Roll No 20NU1A0472 from

Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag has successfully

Completed One Credit Course with Industry Oriented Training on Embedded Systems

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Visakhapatnam

From 5th March 2022 to 4th June 2022

Place: Visakhapatnam

Date: 16th June 2022



Authorized Signatory

Cdr. Gopikrishna Sivvam, IN(Retd.) Chief Operating Officer-CEMS

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON SKILL-ORIENTED COURSE

The Department of Electronics and Communication Engineering introduced a skill-oriented course PCB Design from 18-10-21 to 30-01-22. It was conducted as per the curriculum

Outcomes of the SOC:

Understand the basics of PCB designing

Apply advanced techniques, skills, and modern tools for designing and fabrication of PCBs

Apply the knowledge and techniques to fabricate Multilayer, SMT, and HDI PCB

Understand the concepts of Packaging

Test the Designed and fabricated PCB

Signature of HoD

Head of the Doppringent Dept. of Electronics & Communication Energy M.S.Raju Institute of Technology Sontyam Visakhapotnam-533 173

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON SKILL-ORIENTED COURSE

The Department of Electronics and Communication Engineering introduced a skill-oriented course Basics of Python Programming from 28-02-22 to 04-06-22. It was conducted as per the curriculum

Outcomes of the SOC:

Demonstrate basic programming in Python

Demonstrate use of data structures and object-oriented programming in Python

Build programs using packages like NumPy, Pandas, SciPy, Matplotlib

Signature of HoD

Head of the Department Dept of Electronics& Communication Ergs -N.S.Raju Institute of Technology Sontyam, Visekhapernam-531 173 INSTITUTE OF TECHNOLOGY

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING Report on "SAMBHAV - 2K21"

The Department of Electrical & Electronics Engineering, Nadimpalli Satyanarayana Raju Institute of Technology (Autonomous) conducted Guest Lecture "SAMBHAV - 2K21" on 18th November 2021. The resource person was Shri. GVR Naidu, Assistant Director, MSME-DI who has a good exposure in Entrepreneurship. The event was organized to spread awareness on Entrepreneurship development among students of the Electrical and Electronics Engineering department.

The main objective of the campaign is to familiarize the youth future entrepreneurs with the assistance and various schemes initiated by the Government of India, Ministry of MSME to help students develop the business ideas and set up their own ventures.

The event has been conducted for the EEE II, III & IV year, total 90 students and the Faculty, total 08 members. Mr. A. Yellaji, Assistant Professor has hosted the event. The Head of the Department has addressed the students as well as thanked the resource person for accepting the invitation for the conduction of the event.

Initially, the resource person motivated the students with their personal experiences and knowledge regarding the MSME businesses and explained briefly the overall idea of schemes available in India.



Dr. M. A. Khadar Baba, Principal of Institution also addressed the gathering with his wonderful words and suggestions for their career.

The session also involved a great interaction of students from all years of EEE with resource persons regarding various suggestions as well as showing their gratitude.

The session has been concluded by Vote of Thanks by Dr. RSR Krishnam Naidu, HOD, EEE by thanking Shri. GVR Naidu, Assistant Director, MSME-DI for making the event such a resourceful event and requested the faculty members to share their valuable wishes.

The Faculty members also shared their wishes and thanked Shri. GVR Naidu, Assistant Director, MSME-DI for his valuable suggestions to the students.

Reracio_

HOD - EEE Head of the Department Dour of Electrical & Electronics Engg., N S Raja Institute of Technology Autonomous Sontyam, Visakhapatnam - 531 173

PAL PRINCIPAL RAJU INSTITUTE OF TECHNOLOG (AUTONOMOUS) SONTYAM, ANANDA FURAM SAKHAPATNAM-531171

	DEPARTME	NT OF ELECTRICAL ANI	D ELECTRONICS ENGINEERIN	G
	II SEMESTER BASICS (OF POWER SYSTEMS R	EGISTERED STUDENTS FOR A	LY 2021-2022
<u>5.NÖ</u>	Name of the student	Roll Number	Email id	Name of the Course
1	ADAPUREDOI DIVYA	20NU1A0201	20NU1A0201@nanl.edu.in	BASICS OF POWER SYSTEMS
2	ADINULAM BHAGATH	2010/140202	20NU1A0202@nsnt.edu.in	BASICS OF POWER SYSTEMS
3	ALLA POLI VINAY	2014/140203	20NU1A0203@nanl.edu.in	BASICS OF POWER SYSTEMS
4	ALLADI AKSHAY	20NU1A0204	20NU1A0204@nsrc edu in	BASICS OF POWER SYSTEMS
5	APPIKONDA ASWINI	20NU1A0205	20NU1A0205@nerit edu in	BASICS OF POWER SYSTEMS
6	CHAPPA CHUDAMANI	20NU1A0207	20NU1A0207@rort.edu.in	BASICS OF POWER SYSTEMS
2	CHINTHALA JANANI	20NU1A0208	20NU1A0208@nsnt.edu.in	BASICS OF POWER SYSTEMS
8	D TARUN CHANDRA YUWARAJ	20NU1A0209	20NU1A0209@ntrit.edu.in	BASICS OF POWER SYSTEMS
9	DAMMU DINESH	20NU1A0210	20NU1A0210@rsni.edu.ie	BASICS OF POWER SYSTEMS
10	GOLLAVILLI MANI DEEPAK	20NU1A0211	20NU1A0211@nsrt.edu.in	BASICS OF POWER SYSTEMS
11	GORLE SRINIWAS	20NU1A0212	20NU1A0212@nsril.edu.in	BASICS OF POWER SYSTEMS
12	GOTTAPU DIVYA	20NU1A0213	20NU1A0213@nsritedu.in	BASICS OF POWER SYSTEMS
13	ITHAMSETTY JANAROHAN KUMAR	20NU1A0214	20NU1X0214@nsritedu in	BASICS OF POWER SYSTEMS
14	JAKKANA ASHOK	20NU1A0215	20NU1A0215@nstLedu.in	BASICS OF POWER SYSTEMS
15	KALLA VAMSI KRISHNA	20NU1A0216	20NU1A0215@nstt.edu.in	BASICS OF POWER SYSTEMS
16	KARAKA REVATHI	20NU140217	20NU140217@nsittedu in	BASICS OF POWER SYSTEMS
17	KASSEY DELIESH SAI CHARAN	20NU1A0218	20NU1A0218@rsrtt.edu.in	BASICS OF POWER SYSTEMS
58	KENGUVA UMA MAHESWAR	20NU140219	20NU140219@nant.edu.in	BASICS OF POWER SYSTEMS
特	MIRTH PATI LOKESH	20NU1A0220	20NU1A0220@nant.edu.in	BASICS OF POWER SYSTEMS
20	NAKKELLA DIVYA	20NU1A0221	20NU 1A0221@nsrit.edu.in	BASICS OF POWER SYSTEMS
21	NIRUJOGI RAWESH	20NU1A0222	20NU1A0222@rark.edu.in	BASICS OF POWER SYSTEMS
a	PENMADI UDAY SRINIVAS	20NU1A0223	20NU 1A0223@rent.edu.in	BASICS OF POWER SYSTEMS
28	PINISETTI YERM BABY	20NU1A0224	20NU1A0224@rsritedu.in	BASICS OF POWER SYSTEMS
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26	YELLAPU NAGA SOWMYA SREE	20NU1A0227	20NU1A0227@nsnt.eduin	BASICS OF POWER SYSTEMS

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	DEPARTMENT OF EL	ECTRICAL AND	ELECTRONICS ENGINEERING	3
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2	CHELLUBOINA HARI SATYA TEJA	19NU1A0202	19NU1A0202@rsn1 edu in	BASICS OF POWER SYSTEMS
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4	KALLA HARSHAVARDHAN	19NU1A0204	19NU1A0204@rsnLedu 1	BASICS OF POWER SYSTEMS
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23	MIRTHIPATI GANESH KUMAR	20NU5A0213	20NU5A0213@risht.edu.in	BASICS OF POWER SYSTEMS
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This is to certify that Mr /Ms. Jakkana Ashok studying B.Tech. Department having REGD No.20.NUIA0215 From Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag Has successfully completed One Credit Course with Industry Oriented Training on Basics of power Systems

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS),Gandhigram, Visakhapatnam

From 23/10/2021 to 18/ 12/ 2021

Certificate No:

AP/VIZ/ 4002/ 10-2021/0385 A

18/ 12/ 2021

Authorized Signatory

Date:



www.cemsindia.org

Cdr.Gopikrishna Sivvam,IN (Retd.) Chief Operating Officer-CEMS





This is to certify that Mr /Ms. Gottopu Divya studying B.Tech.III. Semester of EEE Department having REGD No. 20NUIA.0213 From Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag Has successfully completed One Credit Course with Industry Oriented Training on Basics of power systems

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From 23/10/2021 to 18/ 12/2021

Authorized Signatory

Certificate No:

Date:

AP/ VIZ/ 4002/ 10-2021/0360

Cdr.Gopikrishna Sivvam, IN (Retd.) Chief Operating Officer-CEMS





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-	IN SEMESTER INDUCTIO	N MOTORS REGISTER	ED STUDENTS FOR A Y 2021-2022	
-NO	Name of the student	Roll Number	Email Id	Name of the Course
1	ADAPUREDDI DIVIYA	20NU1A0201	20NU140201@still.edu.m	INDUCTION MOTORS
2	ADMULAM BHAGATH	20NU1A0202	20NU1A0202@nsnl.edu m	INDUCTION MOTORS
1	ALLA POLI VINAY	20NU1A0203	20NU140203@nsit.edu m	INDUCTION MOTORS
4	APPIKONDA ASWINI	20NU1A0205	20NU1A0205@asiti edu n	INDUCTION MOTORS
5	CHAPPA CHUDAMANI	20NU1A0207	20NU1A0207@nonl.edu m	INDUCTION MOTORS
6	CHINTHALA JANANI	20NU1A0208	20NU1A0208@ssrit.edu in	INDUCTION MOTORS
7	DAMMU DINESH	20NU1A0210	20NU140210@isrt edu n	INDUCTION MOTORS
\$	GOLLAVILLI MANI DEEPAK	20NU1A0211.	20NU1A0211ghsril edu in	INDUCTION MOTORS
è	GORLE SRINIVAS	20NU1A0212	20hU140212@nsril.edu.m	INDUCTION MOTORS
10	GOTTAPU DIVYA	20NU1A0213	20NU*A0213@testl.edu.in	INDUCTION MOTORS
11	THAMSETTY JANARDHAN KUMAR	20NU1A0214	20NU140214@isti edu n	INDUCTION MOTORS
12	JAKKANA ASHOK	20NU1A0215	20NU1A0215@nsrit.edu.in	INDUCTION MOTORS
13	KALLA VAMSI KRISHNA	20NU1A0216	20NU1A0216@tsril.edu.in	INDUCTION MOTORS
14	KARAKA REVATHI	20NU1A0217	20NU140217(Brisnit edu in	INDUCTION MOTORS
15	KASSEY DELIESH SA/ CHARAN	20NU1A0218	20NU1A0218@nc 1.edu n	INDUCTION MOTORS
16	KENGUVA UMA MAHESWAR	20NU1A0219	20NU1A0219@nsnl.edu n	INDUCTION MOTORS
17	WIRTH FATILOKESH	20NU1A0220	20NU140220@nsrit edu.in	INDUCTION MOTORS
18	NAKKELLA DIVYA	20NU1A0221	20NU1A0221@nsrit.edu in	INDUCTION MOTORS
掉	NIRLUOGI RAMESH	20NU1A0222	20MJ/1A0222@ssrit.edu.in	INDUCTION MOTORS
20	PEMWADI UDAY SRINIVAS	20NU1A0223	20NU140223@nsrit.edu.in	INDUCTION MOTORS
21	PINISETTI YERNI BABY	20NU1A0224	20NU1A0224@ssri.edu.in	INDUCTION MOTORS
22	ŞALAPU VASANTHI	20NU140225	20NU1A0225@tsril.edu.m	INDUCTION MOTORS
23	SIMMA YUGANDHAR	20NU140226	20NU1A0226@nsrk.edu.in	INDUCTION MOTORS
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25	BEVARA PRIVANKA	21NU540201	21NU5A0201@rank.edu in	INDUCTION MOTORS
25	BUDDHA RAKESH	21NU5A0202	21NU5A0202@isri.edu in	INDUCTION MOTORS
27	DHARMANA VENKATA SAI RAKESH	21NU540203	21NU5A0203@nsrit.edu.in	INDUCTION MOTORS
28	DODDI SINDHUSHA	21NU540204	21NU5A0204@nsrit.edu.in	INDUCTION MOTORS
29	GALULA NISHANK BABA	21NU540205	21NU5A0205@nsni.edu.in	INDUCTION MOTORS
30	KOPPELA NAGA JAGANNADHA LOKESH VARMA	21NU540206	21NU5A0206@nsnLedu.in	INDUCTION MOTORS
31	NEELAPU CHARAN KUMAR REDOY	21NU540207	21NU5A0207@nsrk.edu.in	INDUCTION MOTORS
32	PALURI SALVENKATA TEJA	21NU540208	21NUSA0208@rark edu in	INDUCTION MOTORS

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	DEPARTMENT OF ELEC	TRICAL AND ELE	CTRONICS ENG. NEERING			
	VI SEMESTER INDUCTION MOTORS REGISTERED STUDENTS FOR A.Y 2021-2022					
S.No	Name of the student	Roll Number	Email id	Name of the Course		
1	BOBBILI VARSHINI SIVA SANTHOSHI	19NU1A0201	19NU1A0201@nsrit.edu.in	INDUCTION MOTORS		
2	CHELLUBOINA HARI SATYA TEJA	19NU1A0202	19NU1A0202@nsrit.edu.in	INDUCTION MOTORS		
3	JONNADA SATYA	19NU1A0203	19NU1A0203@nsrit.edu.in	INDUCTION MOTORS		
4	KALLA HARSHAVARDHAN	19NU1A0204	19NU 1A0204@nsnt.edu.in	INDUCTION MOTORS		
5	KANCHIPATI PRASAD	19NU1A0205	19NU 1A0205@nsrit.edu.in	INDUCTION MOTORS		
6	KOPPOJU SAI BRAHMAJI	19NU1A0207	19NU1A0207@ttsrit.edu.in	INDUCTION MOTORS		
7	LENKA DINESH MANIKANTA	19NU1A0208	19NU 1A0208@nsnt.edu.in	INDUCTION MOTORS		
8	PAVADA ANIL KUMAR	19NU1A0210	19NU1A0210@nsrit.edu.in	INDUCTION MOTORS		
9	RECOIPALLI HIMANSHU	19NU1A0211	19NU1A0211@nsrit.edu.in	INDUCTION MOTORS		
10	SRIKAKULAPU CHINNI HARISH	19NU1A0213	19NU1A0213@nsnt.edu.in	INDUCTION MOTORS		
11	ADARI VARAHA VENKATA JAGADEESWARAMMA	20NU5A0201	20NU5A0201@nsnt.edu.in	INDUCTION MOTORS		
12	AINAMPUDI NARENDRA VARMA	20NU5A0202	20NU5A0202@rsrit.edu.in	INDUCTION MOTORS		
13	BALIBANI PAVAN KUMAR	20NU5A0203	20NU5A0203@rsrit.edu.in	INDUCTION MOTORS		
14	BODIREDDY CHANDRA SEKHAR REDDY	20NU5A0204	20NU5A0204@nsrit.edu.in	INDUCTION MOTORS		
15	BOIDA VIJAYA KUMAR	20NU5A0205	20NU5A0205@nsrit.edu.in	INDUCTION MOTORS		
16	CHODIPILLI VENKATA SATYA MADHU	20NU5A0206	20NU5A0206@nsrit.edu.in	INDUCTION MOTORS		
17	CHUKKALA SRINU	20NU5A0207	20NUSA0207@nsrit.edu in	INDUCTION MOTORS		
18	DARIMISETTI MOULI	20NU5A0208	20NU5A0208@nsrit.edu.in	INDUCTION MOTORS		
19	KAMPARA VENI SRI	20NU5A0209	20NU5A0209@nsrit.edu.in	INDUCTION MOTORS		
20	KANDREGULA JAYANTH	20NU5A0210	20NU5A0210@nsrit.edu.in	INDUCTION MOTORS		
21	KATIPALLI AJAYKUMAR	20NU5A0211	20NU5A0211@nsrit.edu.in	INDUCTION MOTORS		
22	KUNDRAPU ANUSHA	20NU5A0212	20NU5A0212@nsrit.edu.in	INDUCTION MOTORS		
23	MIRTHIPATI GANESH KUMAR	20NU5A0213	20NU5A0213@risrit.edu.in	INDUCTION MOTORS		
24	MYLAPALLI RAMESH	20NU5A0214	20NU5A0214@nsnit.edu.in	INDUCTION MOTORS		
25	PATIBANDLA BOAZ RAJU	20NU5A0215	20NU5A0215@nsrit.edu.in	INDUCTION MOTORS		
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This is to certify that Mr. /Ms. YELLAPU NAGA SOWMYA SREE Studying

B. Tech II Years of EEE Department having Roll No. 20NU1A0227 from

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Completed One Credit Course with Industry Oriented Training on Induction Motors

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Visakhapatnam

From 5th March 2022

to 4th June 2022

Place: Visakhapatnam

Date: 16th June 2022



www.cemsindia.org

Authorized Signatory

Cdr. Gopikrishna Sivvam, IN(Retd.) Chief Operating Officer-CEMS



B.Tech II Years of EEE Department having Roll No 20NU1A0215 from Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag has successfully

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Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Visakhapatnam

From 5th March 2022

to 4th June 2022

Place: Visakhapatnam

Date: 16th June 2022



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Authorized Signatory

Cdr. Gopikrishna Sivvam, IN(Retd.) Chief Operating Officer-CEMS

	DEPARTME	NT OF ELECTR	ICAL AND ELECTRONICS I	INGINEERING
	N SEMESTER SOC PROGRAM	MABLE LOGIC	CONTROLLER REGISTERE	D STUDENTS FOR A.Y 2021-2022
NO.	Name of the student	Roll Number	Email Id	Name of the SOC
1	ADAPUREDD DIWA	20NU1A1201	20NU140201@rarit edu in	PROGRAMMABLE LOGIC CONTROLLER
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4	ALLAD AND MY	20NJ1A2204	20NU1A0204@risitiedu.in	PROGRAMMABLE LOGIC CONTROLLER
5	APPN DIDA ASWINI	20MU1A3205	20MU1A0205@nort.edu.in	PROGRAWMABLE LOGIC CONTROLLER
6	DIA79A DILGANAN	20NU1A1207	20Nu1A0207@visitiedu.in	PROGRAMMABLE LOGIC CONTROLLER
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\$	SAME OF EDI	20NU1A8210	20NU1A0210@vart.edu in	PROGRAMICABLE LOGIC CONTROLLER
10	GOLLAVELI WARD DEEPAN	20NU1A3211	20MJ1A0211@nsrt.edu in	PROGRAMMABLE LOGIC CONTROLLER
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29	DRAMMANA VEHIKATA DA RAKESH	21NU5A0203	21NUSA0203@nart.edu.in	PROGRAMMABLE LOGIC CONTROLLER
30	DOCDI SINCHESHA	21NU5A0204	21NU5A0204@nsrt.edu in	PROGRAMMABLE LOGIC CONTROLLER
31	GANUA HESHANX BASA	21NU540205	21NU5A0205@nurit.edu in	PROGRAMMABLE LOGIC CONTROLLER
12	KOPPELA NASA JASA BADRA LOKESH WARAK	21NU540206	21NU5A0205@nsrit.edu.in	PROGRAMMABLE LOGIC CONTROLLER
33	NEELAPS CHARACTERING RESON	21NUSA0207	21NU5A0207@norit.edu.in	PROGRAMMABLE LOGIC CONTROLLER
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INSTITUTE OF TECHNOLOGY

(Approved by AICTE New Delhi & Permanently Alf Ested to JHTUK, Kakinada)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

REPORT ON ART OF WRITING TECHNICAL PAPER

Department of Electrical and Electronics Engineering Organised A one day seminar on Art of Writing Technical Paper On 6th August 2022 with Dr.Bharani Chandra Kumar,Professor,GITAM University.

Topics delivered by Resource person:

Zoom Meeting Link:

https://us06web.zoom.us/j/83569921028?pwd=L3FNNTd5cGVjV0tuSmlTa2IPU0NSUT09

This Event was conducted through online mode and all third year students (32) and all the department faculty members are participated and got benirited. The expected final outcome from all the participents are to publish in scopus indexed journal with minimum plagrisium check.



HOD of EEE Dr.R.S.R.Krishnam Naidu Concludes the session with vote of thanks.

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Webinar on CAREER PATH -THINKING Department of CSE

Speaker : Mr. K. Babu Rajendra Kumar

Director – Engineering at Freshworks

About the Speaker: Have 22+ years' work experience in Software development in companies like GEP, Microsoft, Amazon, Oracle, Exeter, Omnesys. Executed Leadership roles in data sciences, analytics and data engineering with focus on product development in areas like CRM, OEM, Analytics, Supply Chain, Logistics, Reverse Logistics, Command Centre, ERP, Education domains.

Date : 22.2.22, 9.00 AM to 10.00 AM

Audience: 3rd and 4th year CSE students

Total Numbers presented: 100

Summary :

Mr. Babu addressed the students about What to choose, How to choose, and Achieve student's goals with a systematic and planned approach.

What to choose: Explained the varieties of software (Application software, Application specific, General Purpose Software and System Software) and their subcategories. Introduced the application domains of Data Science and Machine Learning. Gave the importance of updating oneself as software industry is rapidly changing it's use of technologies. Presented contrast of traditional and modern Software Development Lice Cycle Model (SDLC). This gave clear picture that one should be updated with cutting edge technologies. Presented Periodic table of Software. Explained the importance of knowing all these.

How to choose: Based on the students' interest and skills, one may choose to be front end developer, back end developer, Cloud architect/Associate, industrial designer or Data scientist/ML Engineer.

Achieve: Career Progress -- Think, Organize, Work on it, Launch and Achieve.

Finally answering one of the questions, he also explained how to prepare and crack SDE jobs at FAANG like companies. The most important thing is – **the confidence and the articulation of the solution** that is presented gets a student his/her dream job.



















Codathon March 2022

Department of CSE

Department of CSE has conducted a codathon, a logic building and programming skills development contest on March 2022. Totally 115 students participated from 4 institutes.

The Questions hosted for the contest, students marks reports are attached herewith.

Co-ordinator: Dr. V. Sreerama Murthy Institutes participated: NSRIT, ANITS, GMRIT, and GVP.

NSRIT March Codathon 2022





Given an array of integers, without reordering, determine the maximum difference between any element and any prior smaller difference. If there is never a lower prior element, return -1.

Example

arr = [5, 3, 6, 7, 4]

There are no earlier elements than *arr[0]*. There is no earlier reading with a value lower than *arr[1]*. There are two lower earlier readings with a value lower than *arr[2] = 6*:

- arr[2] arr[1] = 6 3 = 3
- arr[2] arr[0] = 6 5 = 1

There are three lower earlier readings with a lower value than *arr[3] = 7:*

- arr[3] arr[2] = 7 6 = 1
- arr[3] arr[1] = 7 3 = 4
- arr[3] arr[0] = 7 5 = 2

There is one lower earlier reading with a lower value than *arr[4] = 4*:

• arr[4] - arr[1] = 4 - 3 = 1

The maximum trailing record is *arr[3] - arr[1] = 4*.

Example

arr = [4, 3, 2, 1]

No item in arr has a lower earlier reading, therefore return -1

Function Description

Complete the function *maximumTrailing* in the editor below.

maximumTrailing has the following parameter(s): int arr[n]: an array of integers

Returns:

int: the maximum trailing difference, or *-1* if no element in *arr* has a lower earlier value

Constraints

- $1 \le n \le 2 \times 10^5$
- −10⁶ ≤ arr[i] ≤ 10⁶ and 0 ≤ i < n

▼ Input Format For Custom Testing

Input from stdin will be processed as follows and passed to the function:

The first line contains a single integer, *n*, the number of elements in the array *arr*.

Each of the *n* subsequent lines contains a single integer, each an element arr[i] where $0 \le i < n$.

▼ Sample Case 0

Sample Input 0

STDIN		Function
7	\rightarrow	arr[] size $n = 7$
2	\rightarrow	arr = [2, 3, 10, 2, 4, 8, 1]
3		
10		
2		
4		
8		
1		

Sample Output

8

Explanation

Differences are calculated as:

- 3 [2] = [1]
- 10 [3, 2] = [7, 8]
- 4 [2, 3, 2] = [2, 1, 2]
- 8 [4, 2, 3, 2] = [4, 6, 5, 6]

The maximum trailing difference is 10 - 2 = 8.

▼ Sample Case 1

Sample Input 1

```
STDIN Function

------

6 \rightarrow arr[] size n = 6

7 \rightarrow arr = [7, 9, 5, 6, 3, 2]

9

5

6

3

2
```

Sample Output

2

Explanation

Differences are calculated as:

- 9 [7] = 2
- 6 [5] = 1

The maximum trailing difference is 2.

Question - 2 Suspicious Activity From Logs

SCORE: 50 points
Application logs are useful in analyzing interaction with an application and may also be used to detect suspicious activities.

A log file is provided as a string array where each entry represents a money transfer in the form "*sender_user_id recipient_user_id amount*". Each of the values is separated by a space.

- sender_user_id and recipient_user_id both consist only of digits, are at most 9 digits long and start with non-zero digit
- *amount* consists only of digits, is at most 9 digits long and starts with non-zero digit

Logs are given in no particular order. Write a function that returns an array of strings denoting user_id's of suspicious users who were involved in at least *threshold* number of log entries. The id's should be ordered ascending by numeric value.

Example

logs = ["88 99 200", "88 99 300", "99 32 100", " 12 12 15"] threshold = 2

The transactions count for each user, regardless of role are:

```
ID Transactions
-- ------
99 3
88 2
12 1
32 1
```

There are two users with at least *threshold = 2* transactions: *99* and *88*. In ascending order, the return array is *['88', '99']*.

Note: In the last log entry, user *12* was on both sides of the transaction. This counts as only *1* transaction for user *12*.

Function Description

Complete the function *processLogs* in the editor below.

The function has the following parameter(s):

string *logs[n]*: each *logs[i]* denotes the ith entry in the logs

int *threshold*: the minimum number of transactions that a user must have to be included in the result

Returns:

string[]: an array of user id's as strings, sorted ascending by numeric
value

Constraints

- $1 \le n \le 10^5$
- $1 \leq threshold \leq n$
- The *sender_user_id*, *recipient_user_id* and *amount* contain only characters in the range ascii['0'-'9'].
- The sender_user_id, recipient_user_id and amount start with a nonzero digit.
- 0 < length of sender_user_id, recipient_user_id, amount ≤ 9.
- The result will contain at least one element.

▼ Input Format Format for Custom Testing

Input from stdin will be processed as follows and passed to the function.

The first line contains the integer, *n*, the size of *logs*. The following *n* lines contain a string, *logs[i]*. The last line contains an integer, *threshold*.

▼ Sample Case 0

Sample Input

STDIN Function -----4 → logs[] size n = 4 1 2 50 → logs = ["1 2 50", "1 7 70", "1 3 20", "2 2 17"] 1 7 70 1 3 20 2 2 17 2 → threshold = 2

Sample Output

1 2

Explanation

ID	Transactions
1	3
2	2
7	1
3	1

Only users 1 and 2 have at least *threshold* = 2 transactions. The return array in numerically ascending order is ["1", "2"]. Note that in the last log entry, the user with id 2 performed both roles in the transaction. This is counted as one transaction for the user.

▼ Sample Case 1

Sample Input

Sample Output

7

Explanation

Only user 7 has 3 or more transactions. The return array is ["7"].

Question - 3 K-Subarrays	2	SCORE: 75 points		
Medium Math	Dynamic Programming	Algorithms	Problem Solving	

A *k-subarray* of an array is defined as follows:

- It is a subarray, i.e. made of contiguous elements in the array
- The sum of the subarray elements, s, is evenly divisible by k, i.e.: sum mod k = 0.

Given an array of integers, determine the number of *k*-subarrays it contains. For example, *k* = 5 and the array nums = [5, 10, 11, 9, 5]. The 10 *k*-subarrays are: {5}, {5, 10}, {5, 10, 11, 9}, {5, 10, 11, 9, 5}, {10}, {10, 11, 9}, {10, 11, 9, 5}, {10, 11, 9, 5}, {10, 11, 9, 5}.

Function Description

Complete the function *kSub* in the editor below. The function must return a long integer that represents the number of *k-subarrays* in the array.

kSub has the following parameter(s):

k: the integer divisor of a k-subarray nums[nums[0],...nums[n-1]]: an array of integers

Constraints

- $1 \le n \le 3 \times 10^5$
- $1 \le k \le 100$
- 1 ≤ nums[i] ≤ 10⁴

Input Format For Custom Testing

Input from stdin will be processed as follows and passed to the function.

The first line contains an integer, *k*, the number the sum of the subarray must be divisible by.

The next line contains an integer, *n*, that denotes the number of elements in *nums*.

Each line *i* of the *n* subsequent lines (where $0 \le i < n$) contains an integer that describes nums[i].

▼ Sample Case 0

Sample Input For Custom Testing Sample Input 0

Sample Output 0

4

Explanation 0

The 4 subarrays of *nums* having sums that are evenly divisible by k = 3 are $\{3\}$, $\{1, 2\}$, $\{1, 2, 3\}$, $\{2, 3, 4\}$.

Question - 4 Count String Permutations

SCORE: 75 points

 Dynamic Programming
 Medium
 Algorithms
 Problem Solving
 Interviewer Guidelines

Find the number of strings of a given length that can be formed under the following rules:

- Each letter is a vowel, that is, it is in the set {a, e, i, o, u}.
- The letter *a* may only be followed by the letter *e*.
- An *e* may only be followed by an *a* or an *i*.
- An *i* may not be next to another *i*.
- The letter *o* may only be followed by an *i* or a *u*.
- The letter *u* may only be followed by an *a*.

Example

To illustrate some of the rules, start with the string s = 'a' and build to the right.

- 1. 'a' may only be followed by 'e', so the new string can be 'ae'.
- 2. 'ae' may only be followed by 'a' or 'i, so the new string can be 'aea' or 'aei'.
- 3. 'aea' must be 'aeae' next, and 'aei' can be 'aeia', 'aeie', 'aeio', or 'aeiu' because an 'i' cannot follow another 'i'.

Analyses of lengths of strings up to 3 are in the samples below. Since the number of permutations might be very large, return the value modulo $(10^9 + 7)$.

Function Description

Complete the *countPerms* function in the editor below.

countPerms has the following parameter(s):
 int n: the length of string to analyze

Returns:

int: the number of permutations, modulo $(10^9 + 7)$

Constraints

• $0 < n < 10^5$

Input Format For Custom Testing

Input from stdin will be processed as follows and passed to the function.

The only line contains an integer, *n*, the length of the string to analyze.

▼ Sample Case 0 Sample Input STDIN Function 1 → length of string to analyze n = 1

Sample Output

5

Explanation

There are 5 strings of length 1, each containing a single vowel. 5 mod $(10^9+7) = 5$.

▼ Sample Case 1										
Sample Input										
STDIN		Function	1							
2	\rightarrow	length c	f	string	to	analyze	n	=	2	

Sample Output

10

Explanation

There are 10 strings of length 2: {'io', 'iu', 'oi', 'ou', 'ua', 'ae', 'ea', 'ei', 'ia', 'ie'}.

10 mod (10⁹+7) = 10

▼ Sample Case 2

Sample Input

STDIN Function
----3 → length of string to analyze n = 3

Sample Output

19

Explanation

There are 19 strings of length 3: {'iua', 'oia', 'oie', 'oio', 'oiu', 'oua', 'uae', 'aea', 'aea', 'eia', 'eie', 'eio', 'eiu', 'iae', 'iea', 'iei', 'ioi', 'iou'}. 19 mod $(10^9+7) = 19$

Medium Data Stru	uctures Binary Tre	es Algorithms	Problem Solving
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A *binary tree* uses a multi-node data structure where each node may have 0 to 2 child nodes, and has one stored *value*, its node number in this case. A tree may either be:

- An empty tree, the root is *null*.
- A non-empty tree with a non-null root node that contains a *value* and up to 2 subtrees, *left* and *right*, which are also binary trees.

A binary tree is classified as a *binary search tree (BST)* if all of the nonnull nodes exhibit two properties:

- The left subtree of each node contains only nodes with *values* that are lower than its own *value*.
- The right subtree of each node contains only nodes with *values* that are higher than its own *value*.

A *pre-order traversal* is a recursive tree traversal method where the *current node* is visited first, then the *left subtree*, and then the *right subtree*. The following pseudocode parses a tree into a list using pre-order traversal:

- If the root is null, return the null list.
- For a non-null root node:
 - 1. Create a pre-order traversal list, *left*, of the left subtree.
 - 2. Create a pre-order traversal list, *right*, of the right subtree.
 - 3. Return the concatenated list: the non-null node + *left* + *right*.

Given a pre-order traversal history of a binary tree, check whether the path represents a valid BST or not. Return a string, either *YES* if the path can represent a valid BST, or *NO* if it cannot.

Example

nodes = [2, 1, 3, 4, 5]

- The root node will always be the first node in the array. In this case, the root is at node 2
- The next value, 1 is less than 2. Place the node 1 at the left of node 2.
- The next value, 3 is greater than 2. Place the node 3 at the right of node 2.
- The next value, 4 is greater than 3. Place the node 4 at the right of node 3.
- The next value, 5 is greater than 4. Place the node 5 at the right of node 4.
- This graph meets the definition of a BST.



Function Description

Complete the *isValid* function below.

isValid has the following parameter:

int a[n]: An array of integers, the *values* in the order encountered in the traversal of the tree.

Returns:

string: either the string *YES* if the path represents a valid BST, or *NO* otherwise

Constraints

- $1 \le q \le 10$
- 1 ≤ n, a[i] ≤ 100

Input Format

The following parameters must be read from stdin:

The first line contains an integer *q*, the number of queries nodes. The next *q* sets of lines are defined as:

The first line contains an integer *n*, the number of nodes in the tree.

The next line contains a[n]: a list of space-separated integers that denote values encountered in the traversal of a tree

▼ Sample Case 0

Sample Input 0

STDIN	Function				
5	\rightarrow number of queries q = 5				
3	\rightarrow a[] size n = 3 (query 0)				
1 3 2	\rightarrow a = [1, 3, 2]				
3	\rightarrow a[] size n = 3 (query 1)				
2 1 3	\rightarrow a = [2, 1, 3]				
6	\rightarrow a[] size n = 6 (query 2)				
3 2 1 5 4 6	\rightarrow a = [3, 2, 1, 5, 4, 6]				
4	\rightarrow a[] size n = 4 (query 3)				
1 3 4 2	\rightarrow a = [1, 3, 4, 2]				
5	\rightarrow a[] size n = 5 (query 4)				
3 4 5 1 2	\rightarrow a = [3, 4, 5, 1, 2]				

Sample Output 0

YES YES NO NO

Explanation 0

The diagrams below show BST representations with green nodes at valid locations and red at invalid.



An explanation of the q = 5 queries:

- 1. Diagram (a) is valid, so return the string YES.
- 2. Diagram (b) is valid, so return the string YES.
- 3. Diagram (c) is valid, so return the string YES.
- 4. Diagram (d), the query 1 3 4 2, is not valid. The root is 1 because it is the first value in the list. The second value of 3 must be the right child of 1 because it is greater. Likewise, the third value, 4, must be the right child of 3. For 2 to be the last value in the traversal, it has to be the left child of 4. It is less than the root value 3 above it and is on its right subtree. Return the string NO.
- 5. Diagram (e), the query 3 4 5 1 2, is not valid. The root, the first value in the list, is 3. The second value, 4, must be the right child of 3. The third value, 5, must be the right child of 4. For the fourth value to be 1, it must be the left child of 5, but that is less than the root at 4 and is in its right subtree. Return the string NO.

Questio Ancestral	n - 6 Names			SCORE: 75 points
Medium	Strings	Implementation	Problem Solving	

10/16

Given a list of strings comprised of a name and a Roman numeral, sort the list first by name, then by the decimal value of the Roman numeral. In Roman numerals, a value is not repeated more than three times. At that point, a smaller value precedes a larger value to indicate subtraction. For example, the letter I represents the number 1, and V represents 5. Reason through the formation of 1 to 10 below, and see how it is applied in the following lines.

- I, II, III, IV, V, VI, VII, VIII, IX, and X represent 1 through 10.
- XX, XXX, XL, and L are 20, 30, 40, and 50.
- For any other two-digit number < 50, concatenate the Roman numeral(s) that represent its multiples of ten with the Roman numeral(s) for its values < 10. For example, 43 is 40 + 3 = 'XL' + 'III' = 'XLIII'

Example

names = ['Steven XL', 'Steven XVI', 'David IX', 'Mary XV', 'Mary XIII', 'Mary XX']

The result with Roman numerals is the expected return value. Written in decimal and sorted, they are ['David 9', 'Mary 13', 'Mary 15', 'Mary 20', 'Steven 16', 'Steven 40']. The return array is ['David IX', 'Mary XIII', 'Mary XV', 'Mary XX', 'Steven XVI', 'Steven XL'].

Function Description

Complete the function *sortRoman* in the editor below.

sortRoman has the following parameter:

names[n]: an array of strings comprised of names and roman
numerals

Returns:

string[n]: an array of strings sorted first by given name, then by ordinal

Constraints

- 1 ≤ n ≤ 50
- Each names[i] is a single string composed of 2 space-separated values: givenName and romanNumeral.
- romanNumeral represents a number between 1 and 50, inclusive.
- 1 ≤ |givenName| ≤ 20
- Each givenName starts with an uppercase letter ascii[A-Z] which is followed by lowercase letters ascii[a-z].
- There is a space between givenName and romanNumeral
- Each names[i] is distinct.

Input Format for Custom Testing

Input from stdin will be processed as follows and passed to the function.

The first line contains an integer *n*, the size of the array *names*. Each of the next *n* lines contains an element *names[i]*.

Sample Case 0

Sample Input

```
STDIN Function
----- -----
2 → names[] size n = 2
Louis IX → names = ['Louis IX', 'Louis VIII']
Louis VIII
```

Sample Output

```
Louis VIII
Louis IX
```

Explanation

Sort first by *givenName* then, if *givenName* is not unique, by the value of the Roman numeral. In decimal, the list is sorted ['Louis 8', 'Louis 9'].

▼ Sample Case 1

Sample Input

```
STDIN Function
----- ----
2 → names[] size n = 2
Philippe I → names = ['Philippe I', 'Philip
II']
Philip II
```

Sample Output

Philip II Philippe I

Question - 7 Permutations Divisible by 8

SCORE: 100 points

Number Theory	Math	Algorithms	Problem Solving	Hard
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Given an integer string, create all integer permutations of its digits. Determine if there is a permutation whose integer value is evenly divisible by 8, i.e. (*permutation value*) mod 8 = 0.

For example, the possible permutations of 123 are $p = \{123, 132, 213, 231, 312, 321\}$. Of these values, p[4] = 312 is divisible by 8 because 312 mod 8 = 0.

Function Description

Complete the function *checkDivisibility* in the editor below. The function must return an array of *result* strings, either *YES* or *NO*, where each *result[i]* denotes whether a permutation of *arr[i]* is divisible by *8*.

checkDivisibility has the following parameter(s):
 arr[arr[0],...arr[n-1]]: an array of integer strings

Constraints

- 1 ≤ n ≤ 45
- $0 \leq arr[i] \leq 10^{110}$
- All characters of $arr[i] \in \{0-9\}$

Input Format for Custom Testing

Input from stdin will be processed as follows and passed to the function.

The first line contains an integer *n*, the size of the array *arr*. Each of the next *n* lines contains an integer as a string, *arr[i]*, where $0 \le i < n$.

▼ Sample Case 0

Sample Input 0

STDIN Function Parameters
----2 → arr[] Size = 2
61 → arr[] = [61, 75]
75

Sample Output 0

YES NO

Explanation 0

Check the following *n* = 2 values:

- *arr[0] = 61*. The permutation *p* = *16* is divisible by *8* so store *YES* in index *0* of the return array.
- arr[1] = 75. The only permutations are p = 75 and p = 57, but neither of them is divisible by 8. Store NO in index 1 of the return array.

Question - 8 Super Stack	SCORE: 100 points

 Hard
 Algorithms
 Problem Solving
 Data Structures
 Stacks

Implement a *stack* that accepts the following commands and performs the operations described:

- *push v*: Push integer *v* onto the top of the stack
- *pop*: Pop the top element from the stack
- inc i v. Add v to each of the bottom i elements of the stack

After each operation, print the value at the top of the stack. If the stack is empty, print the string '*EMPTY*'.

Example

operations = ['push 4', 'push 5', 'inc 2 1', 'pop', 'pop']



Expected output:

Function Description

Complete the *superStack* function in the editor below. After each operation, print the value of the stack's *top* element on a new line. If the stack is empty, print *EMPTY* instead.

superStack has the following parameter(s):

string operations[n]: an array of strings that represent operations on the stack

Prints:

string: the value of the stack's *top* element; if the stack is empty, print '*EMPTY*', no return value is expected

Constraints

- $1 \le n \le 2 \times 10^5$
- $-10^9 \le v \le 10^9$
- 1 ≤ i ≤ |S|, where |S| is the size of the stack at the time of the operation.
- It is guaranteed that *pop* is never called on an empty stack.

Input Format for Custom Testing

Input from stdin will be processed as follows and passed to the function.

The first line contains an integer *n*, the size of the array *operations*. The next *n* lines each contain a string, *operations[i]*.

▼ Sample Case 0

Sample Input

```
STDIN Function
----- -----
12 → operations[] size n = 12
push 4 → operations = ['push 4', 'pop', 'push
3', 'push 5', 'push 2', 'inc 3 1', 'pop', 'push
1', 'inc 2 2', 'push 4', 'pop', 'pop']
pop
push 3
push 5
push 2
inc 3 1
pop
```

14/16

push 1 inc 2 2 push 4 pop pop

Sample Output

4		
EMPTY		
3		
5		
2		
3		
6		
1		
1		
4		
1		
8		

Explanation

The diagram below depicts the stack after each operation:



After performing each operation, print the value at the top of the stack on a new line.

Start with an empty stack, *S*, expressed as an array where the lowest indexed element is the *bottom* of the stack and the highest is its *top*. Perform n = 12 operations as given:

- 1. *push 4*: Push *4* onto the stack, so *S* = [4]. Print the top element, *4*, on a new line.
- pop: Pop the top element from top of the stack, so S = []. Print 'EMPTY' on a new line.
- 3. *push 3*: Push 3 onto the stack, *S* = [3]. Print 3, and the top of the stack after each of the following operations.
- 4. *push 5*: Push *5* onto the stack, *S* = *[3, 5]*.
- 5. *push 2*: Push *2* onto the stack, *S* = [*3*, *5*, *2*].
- 6. *inc 3 1*: Add *v* = 1 to the bottom *i* = 3 elements of the stack, *S* = [4, 6, 3].
- 7. *pop*: Pop the top element from the stack, S = [4, 6].
- 8. *push 1*: Push *1* onto the stack, *S* = *[4, 6, 1]*.

- inc 2 2: Add v = 2 to bottom i = 2 elements of the stack, S = [6, 8, 1].
- 10. *push 4*: Push *4* onto the stack, *S* = [6, 8, 1, 4].
- 11. *pop*: Pop the top element from the stack, *S* = [6, 8, 1].
- 12. *pop*: Pop the top element from the stack, *S* = [6, 8].

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NSRIT SIH INTERNAL HACKATHON

REPORT

The management and faculty members were excited for the overwhelming response from the students for participating in the Internal Hackathon that was conducted on the 28th and 29th of March 2022 at Nadimpalli Satyanarayana Raju Institute of Technology (Autonomous), Sontyam, Visakhapatnam. In the Internal Hackathon, there was a total participation of 16 teams with 96 students as total strength. Out of this, 68 were male while the remaining 28 were female candidates.

The students have chosen the problem statements from various ministries. Several teams came up with very innovative solution proposals for the given problem statements with the inclusion of domain buckets. Students have developed innovative thinking, team playing, problem solving strategies and so on.





NADIMPALLI SATYANARAYANA RAJU INSTITUTE OF TECHNOLOGY (AUTONOMOUS)

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A Report Of 3Day's Work Shop cum Training Programme On AWS (June 30th, July 1st & July 2nd, 2022)

Venue : Block –III, CSE Seminar Hall-NSRIT, Sontyam, Visakhapatnam Organized by: Department of CSE in Association with Brain O Vision and Computer Society of India (CSI) –NSRIT Student Branch

The Department of Computer Science and Engineering of Nadimpalli Satyanarayana Raju Institute of Technology has organized a Three-Day Workshop for Students of III B.Tech CSE during 30th June to 2nd July, 2022. Sessions were delivered by Mr. R Nagendra, Trainer Brain O Vision Solutions pvt Ltd, Hyderabad and Brief Introduction by Mr. Shankar, Assoc Professor, SBC Coordinator, Dept of CSE-NSRIT, Professor & Head of CSE, Dr. R srinivasa Rayudu.

Topics

- Classic Data Center
- Virtualization
- Cloud and Cloud Computing
- Cloud Computing Service Models
- Cloud Computing Deployment Models
- Service Comparison: AWS, Azure, and GCP
- Amazon Web Services (AWS) and its Benefits

- AWS Global Infrastructure
- AWS Regions and Replication of data between the Regions
- Availability Zones and High Availability
- PoP Locations
- Signup an AWS Free Tier Account
- Different Amazon Web Services
- Ways to access AWS: CLI, Console, and SDKs
- Explore Management Console and Configure AWS CLI
- AWS CloudShell

Hands-On:

- Sign-up for AWS free-tier account
- Explore Console and Configure CLI





Some rules of brainstorming: Encourage wild ideas

- Build on the ideas of others.
- Stay focused.
- One conversation at a time.
- Be visual.
- Thinking process to reframe the problem: Rethink about the question.

Brainstorm bad ideas and unpack your assumptions.

A total of 120 students from III B.Tech Computer Science and Engineering have participated in the Workshop. Mr. Sheik Jani and Mr. Srinivasa Rao Assistant Professor, Department of Computer Science and Engineering organized this workshop. The resource persons are experienced trainers from Brain O Vision, Hyderabad. The feedback on participants are understanding and learning resource person's knowledge, teaching and organization of the workshop, encouraged to organize many workshops like this in future.

Overall, the event was successful.



Department of Mechanical Engineering

THREE-DAY FACULTY DEVELOPMENT PROGRAMME

08 - 10, JULY 2021 10:00 AM ONWARDS

TECHNOLOGICAL ADVANCEMENTS IN NAVAL, DEFENSE & SPACE APPLICATIONS: AN INTEGRATED INDUSTRY 5.0 APPROACH

Model Analysis & Experiemntal Validation of Aerospace Applications, Engineering of Composite in Naval Applications, Control of Vibrations and under water noise for defense stealth Applications

CONVENOR (S)

DR. RAGHU RAM MOHAN REDDY, PROFESSOR OF MECH. ENGG. DR. V. V. RAVI KUMAR, ASSOCIATE PROFESSOR OF MECH. ENGG.

for your information, please write to the following email ID. Last date: July 07, 2021 Registration can be done through google form available in the official website

ravikumar.me@nsrit.edu.in | -91 99943 09783 | www.nsrit.edu.in

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	Sent Crafis			DEFENSE & SPACE APPLICATIONS: AN INTEGRATED INDUSTRY 5.9 APPRDACH" fro Here we ellecting the login credentials and WhatsApp link for batter communication.	erm (Alli-10, July).	2021.			
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Organized by

Department of Mechanical engineering

Three Days National Level Faculty Development Programme on Technological Advancements in Naval, Defense and Space Applications - An integrated Industry 5.0 Approach 08-10th July-2021

Department of Mechanical Engineering has conducted three days National level FDP on "Technological Advancements in Naval, Defence and Space Applications- An integrated Industry 5.0 Approach". First day (08-07-2021), Lecture by chief guest Dr. P. Anil kumar, Scientist F & Director, Research Center, Imarat, DRDO, Hyderabad. Guest lecture on (14.00pm- 16.00pm), Modal analysis & experimental validation of Aerospace applications, Second Day (09-07-2021) Second Guest Lecture by Dr. U. Urban Kumar, Scientist ' D ' NSTL, VSKP. Guest lecture on (14.00pm – 16.00pm), Engineering of composites for navy. Third Day (10-07-2021) Third Guest Lecture by Dr. V. Rama Krishna, Scientist ' E ' NSTL, VSKP. Guest lecture on (14.00pm – 16.00pm), Control of Vibrations and under water noise for defense stealth Applications. In valedictory certificates distributed to the participants faculties from Mechanical department were participated in the workshop.

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				Dr. V. V. RAVI KUMAR, a treat the						
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AUTONOMOUS | ACCREDITED BY NAAC WITH 'A' GADE MECHANICAL ENGINEERING



ENGINEER'S DAY Celebrations

FLIGHT CONTROL ACTUATORS

SEPTEMBER 15, 2021, 04:30 PM

SPEAKER MR. JAI PRAKASH SCIENTIST - E IFCS DIRECTORATE AERONAUTICAL DEVELOPMENT AGENCY BANGALURU, KARNATAKA

Recourse person's brief bio-data:

- 1. Graduated from JNTU College of Engineering in Mechanical engineering
- 2. Holds a PGDCA in E Commerce from JNTU
- 3. Master of Engineering from Indian Institute of Science, Bangalore

Started career as Maintenance Engineer in NFCL, Kakinada, AP

Thereafter served as Jr. Works Manager in Indian Ordnance Factories. Designed and Developed several products under in-house R&D scheme for Indian Armed forces, viz. Remote control weapon stations, Armoured vehicles, Close Range Naval Gun, etc

Since 2008, have been serving in one of DRDO's autonomous bodies meant for design and development of Fighter Aircrafts, viz. Aeronautical Development Agency

Topic: A preliminary understanding of Flight Control Actuators and their evolution.



Email: jaiprakash@jetmail.ada.gov.in



Participants:

From NSRIT – Mechanical Engineering Department – III year – 60 Students

All Mechanical Engineering Department faculty- 16 Faculty

The resource person covered the basics of Mechanical Engineering and Electrical Engineering and correlate the applications of commercial flights and fight flights.



NADIMPALLI SATYANARAYANA RAJU INSTITUTE OF TECHNOLOGY NAAG

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DEPARTMENT OF MECHANICAL ENGINEERING NX CAD ESSENTIALS Registered List

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3	20NU1A0303	CHANDA JAGADEESH KUMAR
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5	20NU1A0305	DARA VIVEK
6	20NU1A0307	DASARI SRAVANA LAKSHMI
7	20NU1A0308	DIVYA PRAKASH KUMAR
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19	20NU1A0320	KINCHA SHYAM KRISHNA
20	20NU1A0322	KOKKERLAPATI SUDHEEP VARMA
21	20NU1A0323	KOLA VENKATA RAO
22	20NU1A0325	KORADA SAI PRASAD
23	20NU1A0326	KUTCHARLAPATI CHANDRAMOULI VARMA
24	20NU1A0327	M GEETA SAI PRASAD
25	20NU1A0328	MAJJI JOGESH
26	20NU1A0329	MASADA DIVAKAR
27	20NU1A0330	MUMMANA DINESH
26	20NU1A0331	MUNJETI YOGENDRA
29	20NU1A0332	NAKKA NAVEEN
30	20NU1A0333	NERELLA DURGA PRASAD
31	20NU1A0335	PENTAKOTA DEVI SIVA PRASAD
32	20NU1A0336	PILLA NAVEEN
33	20NU1A0337	PITLA NAVEEN
34	20NU1A0338	PONTHAPALU YAJNESWAR
35	20NU1A0339	RAYAVARAPU SAI KIRAN
36	20NU1A0340	RANGASALA ARUNXUMAR

	37	20NU1A0341	SEELA LAKSHMI CHANDRA EKANTH
	38	20NU1A0342	SIMMA MOHAN KUMAR
	39	20NU1A0343	SIRIPURAPU MANOJ KUMAR
	40	20NU1A0344	SOURASISH TALUKDER
	41	20NU1A0346	TEDLAPU LIKHITH V S G B SARAN
	42	20NU1A0347	TEEGALA PRUDHVI GUPTA
	43	20NU1A0348	TIRUMAREDDY RAJESH
	44	20NU1A0349	VIJANAGIRI MANI VARA PRASAD
ļ	45	20NU1A0350	YALLA ROHITH
	46	20NU1A0351	YANDRAPU JAGADEESH
	47	20NU1A0352	YEDURU SAMPATH SAI
	48	19NU1A0322	IN RAJEEV LOKESH
	49	19NU1A0323	PYLA RAJA
	50	19NU1A0325	SARAGADAM RAJ KUMAR
30	51	20NU5A0325	DOGGA BALAVENKATA KISHOR
	52	20NU5A0357	MIRTHIPATI SUDREER
	53	20NU5A0362	NASRAT BHANU
	54	20NU5A0366	PATCHIKORA YERRI NAIDU
	55	20NU5A0367	PATNANA VAMSHI
	56	20NU/5A0370	POLIDASU NAGARAJU
	57	20NU5A0373	RAVUPALLI VISWATEJA
	5B	20NU/5A0378	SEKHARAMAHANTI BHARATH KUMAR



NADIMPALLI SATYANARAYANA RAJU **INSTITUTE OF TECHNOLOGY** C.S.C.M.C.P

NÃAC

(AUTONOMOUS)

DEPARTMENT OF MECHANICAL ENGINEERING Mechatronics Registered List

SI.No	Roll.No	Student Name
1	20NU1A0301	AYENAMPUDI NITHIN VARMA
2	20NU1A0302	BANDARU SAGAR
3	20NU1A0303	CHANDA JAGADEESH KUMAR
4	20NU1A0304	DAKETI LEELA SAI KIRAN
5	20NU1A0305	DARA VIVEK
6	20NU1A0307	DASARI SRAVANA LAKSHMI
7	20NU1A0308	DIVYA PRAKASH KUMAR
8	20NU1A0309	DOKKARI MOHAN
9	20NU1A0310	DOPPA ROSHAN SOWRI
10	20NU1A0311	DUVVI PRANEETH VARDHAN
11	20NU1A0312	DWARAPUREDDY VEERA VENKATA SALABHISHEK
12	20NU1A0313	GONTHINA BHASKAR
13	20NU1A0315	GORLE KUSHAL
14	20NU1A0316	GORRIPOTU ANIL KUMAR
15	20NU1A0319	KILARI JAGAN JEEVAN KUMAR
16	20NU1A0320	KINCHA SHYAM KRIŞHNA
17	20NU1A0322	KOKKERLAPATI SUDHEEP VARMA
18	20NU1A0326	KUTCHARLAPATI CHANDRAMOULI VARMA
19	20NU1A0329	MASADA DIVAKAR
20	20NU1A0332	NAKKA NAVEEN
21	20NU1A0333	NERELLA DURGA PRASAD
22	20NU1A0334	PALAKOLLU DILLESWARA RAO
23	20NU1A0335	PENTAKOTA DEVI SIVA PRASAD
24	20NU1A0336	PILLA NAVEEN
25	20NU1A0338	PONTHAPALLI YAJNESWAR
26	20NU1A0339	RAYAVARAPU SAI KIRAN
27	20NU1A0340	RANGASALA ARUNKUMAR
28	20NU1A0341	SEELA LAKSHMI CHANDRA EKANTH
29	20NU1A0342	SIMMA MOHAN KUMAR
30	20NU1A0343	SIRIPURAPU MANOJ KUMAR
31	20NU1A0344	SOURASISH TALUKDER
32	20NU1A0346	TEDLAPU LIKHITH V S G B SARAN
33	20NU1A0347	TEEGALA PRUDHVI GUPTA
34	20NU1A0348	TIRUMAREDDY RAJESH
35	20NU1A0350	YALLA ROHITH
36	20NU1A0351	YANDRAPU JAGADEESH

	37	20NU1A0352 YEQURU SAMPATH SAI							
	30	21NU5A0301	BAKI SANKAR RAO						
- [39	21NU5A0302	BONELA GANESH						
1	40	BONULA PRABHU PAVAN							
1	41	41 21NU5A0304 DODDI UDAY BHASKAR							
1	42	21NU5A0305	GANAGALLA VEERANAND						
	43	43 21NU5A0306 GANTLA ATCHUTH							
	- 44	44 21NUSAD307 GOLAGANI BHANU PRASAD SAI							
	45	21NU5A0308	GUGGILAM RAKESH						
220	46	21NU5A0309	JAGARAPU RAKESH						
	47	21NU5A0310	KARRI SALTEJA						
	48	21NU5A0311 KORADA VENKATESH							
	49	21NU5A0312	MOHAMMAD BASHEERUDDIN						
	50	21NU5A0313	PASANABILLI MOHAN						
	51	21NU5A0314	PENTAKOTA VAYUNANDA SAI KUMAR						
	52	21NU5A0315	PULAMARASETTI GIRIDHAR						
	53	21NU5A0316	SARVASUODI LOKESH						
ľ	54	21NU5A0317	SOURAV DAS						

1.41


This is to certify that Mr. /Ms. BANDARU SAGAR Studying

B.Tech II Year of Mechanical Department having Roll No 20NU1A0302 from

Nadimpalli Satyanaruyana Raju Institute of Technology (NSRIT), Vizog has successfully

Completed One Credit Course with Industry Oriented Training on Mechatronics

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Visakhapatnam

from 5th March 2022 to 4th June 2022

Place:Visakhapatnam

Date: 16th June 2022



Anthorized Signatory

Cdr. Gopikrishna Sivvam, IN(Retd.) Chief Operating Officer-CEMS

and set shall be a set of the set of the



Centre of Excellence in Maritime and Shipbuilding (CEMS) (& Sull Development Initialitys by Gov), of India / Ministry of Shipping / Sagarmala)

This is to certify that Mr /Ms. N. ROJEENi LOKESh. studying B. Tech. W. Semester of Mcchanical Department having REGD No. 19 MULA032 From Nadimpalli Satyanarayana Raju Institute of Technology (NSRIT), Vizag

Has successfully completed One Credit Course with Industry Oriented Training on Essentials for NX Designers

Conducted by

Centre of Excellence in Maritime and Shipbuilding (CEMS), Gandhigram, Visabhapatnam

From 23/10/2021 to 18/12/2021

Certificate No: AP/VIZ/4002/10-2021/0404

Authorized Signatory

Date:



Cdr.Gopikrishna Sivvam, IN (Retd.) Chief Operating Officer-CEMS



DEPARTMENT OF MECHANICAL ENGINEERING

REPORT ON SKILL-ORIENTED COURSE

The Department of Mechanical Engineering introduced a skill-oriented course for B.Tech. III Semester Computer Aided Modelling from 18-10-2021 to 05-02-2022 and B.Tech. IV Semester Computer Numerical Control Programming from 28-02-2022 to 04-06-2022. It was conducted as per the curriculum

Course Outcomes - Computer Aided Modelling (20MES01):

- 1. Recollect the various part designs with 2D and 3D tools.
- 2. Elucidate 3D tools and various assemblies with different approaches.
- 3. Develop automotive parts and assemblies with various sheet metal parameters and constraints.

Course Outcomes - Computer Numerical Control Programming (20MES02):

- Delineate the operation sequence and route sheet for given mechanical parts.
- 2. Exemplify the selection criteria for CNC machines by describing principle, operation, procedural steps for different tooling.
- 3. Load the part program with interface software application for automatic part programming.



Report of the Two-week FDP on Master OBE

Title : Two-week FDP on Master OBE

Dates : 25th June- 5th July, 2021

Venue : Seminar Hall, CSE Block

Convener : Dr. P. N. E. Naveen, Associate Professor, Dept. of Mech Engg.



Inaugurated by Dr. J. Raja Murugadoss, Director and Dr. M.A.Khadar Baba, Principal



Number of Participants : 11

- 1) Dr. K. Madhavi, CSE
- 2) Dr. K. Ravi Kiran, CSE
- 3) Dr. K. Ravi Kumar, ECE
- 4) Mrs. Usha Rani, Mech

- 5) Mr.K. Abhinash, Mech
- 6) Mr. Krishna Kumar, Mech
- 7) Mrs. S. Pavani, CE
- 8) Mr. A. Bala Raja Ram, EEE
- 9) Dr. M. Rahamatulla, S&H
- 10) Dr. E. Madhavi, S&H
- 11) Mr. M. Manoj Kumar, S&H



Da	Dat	Trainer	Modules	Photo
у	e		Covered	
1	25- 6-21	Dr. N.V.V.S.Suryanarayan a	Introduction to OBE, 21 st Century Skills, Basic Philosophy of OBE	
2	26- 6-21	Dr. N.V.V.S.Suryanarayan a	Accreditation , Washington accord, Sydney accord Dublin Accord, PEOs, Mango Analogy	

3	28- 6-21	Dr. N.V.V.S.Suryanarayan a	Continuous Improvement , Feedback and Surveys	
4	29- 6-21	Mrs. V. Usha Rani	Program Outcomes (PO1- PO5)	Southants, Andres Pradent, India Description, Andres Pradent, India Description, Landau Land 17-127-11.4012 Long Edit 12-144520 2005211 10.115 Add
5	30- 6-21	Mrs. V. Usha Rani	Program Outcomes (PO6- PO12)	Bortgant, Andréa Phasael, Inda Bortgant, Andréa Phasael, Inda Bortgant, Andréa Phasael, Inda Bortgant, Andréa Phasael, Inda Bortgant, Andréa Phasael, Inda
6	01- 7-21	Dr. K. S. D. L. Prasad	Revised Bloom's Taxonomy	Serigura Andra Presentine Andra Presentine E2E17.4 mile 1. 2000-000 Button Distances Button Distanc
7	02- 7-21	Dr. K. S. D. L. Prasad	Revised Bloom's Taxonomy	Kunstanten kannen kan

8	03- 7-21	Dr. K. Priya Vyjayanthi	Course Outcomes	Renyam, danska Makan, India Bornancian, Andrea Makan, India Bornancian, Bangar, Bangar
9	05- 7-21	Dr. K. Priya Vyjayanthi	CO- PO Mapping	Congress Congre
10	05- 7-21	Mr. K. Ram Prasad	Curriculum Design	Sections Andrea Hooders Maile Receivers, Andrea Hooders, Maile Mexanol Road, Vanimation Andrea Proceed Sett22A, Meza Lar 12, 260021 Lar 12, 260021 Base 012, 202724 Base 012, 202724
		Dr. P. N. E. Naveen	Hands on Experience	

Video and Objective mode assignments were given and the participants were assessed through presentations . The FDP was concluded on 5th July and participation certificates were distributed.

Dr.P.N.E.Naveen

Dr.N.V.V.S.Suryanarayana

Coordinator, CAE

Coordinator, IQAC

INSERIE NADIMENTUS ATT ANARAYANA RAJU

-	DE	PARTMENT OF	CIVIL ENGINEERING			
III SEM SOC Computer Aided Building Drawing (CAD) REGISTERED STUDENTS LIST for A.Y 2021-2022						
101110	ave an inc student	Roll Number	Email id	Name of the SOC		
	ATTILALIA BALARAJA NARENDRA SHANKAR	20NU1A0101	20MUTAD101@rent.edu.in	Computer Aided Building Drawing		
	BALLAND MANASA	20NU1/0102	20NUTAD102@Envit.edu.m	Computer Aided Building Drawing		
	BOCHINA JAYANTH	20NU1A0103	20NUTA0103@nart.edu.in	Computer Alded Building Drawing		
	BYLAPUDI VIJAY	20NU1A0104	20NU1A0104@nsrit.edu.in	Computer Aided Building Drawing		
0	KOLLA TEJA	20NU1A0106	20NU1A0106@nant.edu.m	Computer Aided Building Drawing		
6	KUNUKU KUSUMA KUMARI	20NU1A0107	20NULAD107@nart.edu.in	Computer Aided Building Drawing		
1	MADHUPADA HEMA	20NU1A0108	20NUTA0108@insrit.edu.in	Computer Alded Building Drawing		
8	MAJJI WINAY PURUSHOTTAM	20NU1A0109	20NU1A0109@nant.edu.in	Computer Aided Building Drawing		
.9	MALLA TEJASAL	20NU1A0110	20NU1A0110/@cont.edu.in	Computer Aided Building Drawing		
10	MEDISETTI AJAY RAM	20NU1A0111	20NU1A0111@nsrit.edu.in	Computer Aided Building Drawing		
11	MUTCHARARLA LAKSHMANUDU	20NU1A0112	20NU1A0112dbnsrit edu in	Computer Aided Building Drawing		
12	NANIREODI EHARATH KUMAR	20NU1A0113	20NUITA0113@natit.edu.in	Computer Alded Building Drawing		
13	PUDI THARUN KUMAR	20NU1A0114	20NRJ1A0114 @caril.edu in	Computer Aided Building Drawing		
34	PYLA DHANUSH KUMAR	20NU1A0115	20NULA0115@bosrit.edu.in	Computer Aided Building Drawing		
15	RANDHI MANISHA	20NU1A0116	20NUTA0116@Dourt.edu.in	Computer Aided Building Drawing		
16	ROKKAM VISWAS	20NU1A0117	20NU1A0117(Dentrit.edu.in)	Computer Aided Building Drawing		
17	RONGALI MANESHANKAR	20NU1A0118	20NULA0118EDnarit adv in	Computer Aided Building Drawing		
插	SALLA KALYAN KUMAR	20NU1A0119	20NRU1A0119@marit.edu.in	Computer Aided Building Drawing		
19	TAMATAPU TARUN	20NU1A0121	20NUTA012120nuril.edu/m	Computer Aided Building Drawing		
20	UPPULLEI SATHWIK RAJU	20NU1A0122	20NULA01228Druit adv m	Computer Alded Building Drawing		
21	VASIREDOLANUSHA	20NU1A0123	20NUTA012320nsrt.edu in	Computer Aided Building Drawing		
22	GUDABA MANOHAR	21NU5A0101	21NU5A0101@maril.edu.m	Computer Aided Building Drawing		
23	KODURU LAXMI PADMAVATHI DEVI	21NU5A0102	21NU5A0102@msrit.edu.in	Computer Alded Building Drawing		
24	KOTHURU UPENDRA	21NU5A0103	21NU5A0103@marit.ettu in	Computer Aided Building Drawing		
25	KUNCHAM V N S S AJAY SRERAM	21NU5A0104	21NU5A0104@marit.edu.im	Computer Aided Building Drawing		
26	RELIVVALA SIVA	21NU5A0105	21NU5A0105@nurit.edu.in	Computer Aided Building Drawing		
27	PONAGANTI DURGA PRASAD	21NU5A0106	21NUSA0106@maril.edu.in	Computer Aided Building Drawing		
28	SHAKEEH	21NU5A0107	21NUSA0107@msrid.edu/im	Computer Aided Building Drawing		
29	TADUTURI LALITHA	21NU5A0108	21NUSA010880murt edu in	Computer Aided Building Drawing		
30	THALLARD REDDY SALKUMAR	21NU5A0109	21NUSA0109@mirit.edu.m	Computer Aided Building Drawing		
31	NO DOADE LOKESH	21NU5A0110	21NUEA0110@msnit.edu in	Computer Aided Building Drawing		
32	ARARA ANIL	21NU5A0111	21NUSA0111@marit.edu.in	Computer Aided Building Drawing		
33	STRUKONOROTHU	21NU5A0112	21NUSA0112@marit.edu.in	Computer Aided Building Drawing		

-12 DEFT COORDINATOR

12 word .-

Head of the Department Dupt, of Civil Engl N.S. Roja Institute of Tucherstone (A Sontyam, Vienknopalnam-5011 CARDING NADIMERALLU SATY ANARASANAG RATE RAJU

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IV SEM SOC BUILDING INFORMATION MODEL INCIDENT DECISTERED STUDENTS LIST for A V 2021-2022					
S.NO	Name of the student	Poll Number	Email Id	Name of the SOC	
1	AYILADA BALARAJA NARENDRA SHANKAR	20MUDA0401	CONTRACTOR STATE	BUILDING INFORMATION MODELING	
2.	BALLANKI MANASA	200101001001	201401A01012Briefle edu in	DUILDING INFORMATION MODELING	
3	BOCHINA JAYANTH	2010140102	2010120102220101.000.00	BUILDING INFORMATION MODELING	
4	BYLAPUDI VUAY	2010140103	20NUTA030302Prism.edu.m	TELM DING INFORMATION MODELING	
5	KOLLA TEJA	2010140104	20NO1X010402Pitrit 604 III	THUS DING INFORMATION MODELING	
6	KUNUKU KUSUMA KUMARI	20101740105	20NUTA310Egerarii edu iri	FULL DING INFORMATION MODELING	
7	MADHUPADA HEMA	2010140107	2050-120107001551,000.01	BUILDING INFORMATION MODELING	
8	MAJJI VINAY PURUSHOTTAM	2010140108	20NUTAOTORIDERO ADD IN	DUILONG INFORMATION MODELING	
9	MALLA TEJASAJ	20/901/01/09	20NG PACIFIC Page and a los	IS IN DING INFORMATION MODELING	
10	MEDISETTI AJAY RAM	2000120110	20NU-X011020mm1 edu in	DUILDING INCORMATION MODELING	
11	MUTCHAKARLA LAKSHMANUPUL	2000100111	20NUTADTITEDININ edu m	COLLARG INCORMATION MODELING	
12	NANIREODI BHARATH KUMAR	20190140112	20NOTA0112220NIT edu lo	BUILDING INFORMATION MODELING	
13	PUDI THARUN KUMAR	20190120113	20NULTAOT 13g mint adu in	BUILDING INFORMATION MODELING	
14	PYLA DHANUSH KUMAR	20581140146	2014UTAUTITED and in	BUILDING INCOMMITION MODELING	
15	RANDHI MANISHA	201401040115	20MOTADS 1582 1911 POLIN	RUL DING INFORMATION MODELING	
16	ROKKAM VIEWAS	2000100110	20NOTHOS TORENTI, HOLLIN	BUILDING INFORMATION MODELING	
17	RONGALI MANI SHANKAR	2000100110	2040140117 201010 00110	BUILDING INFORMATION MODELING	
18	SALLA KALYAN KUMAR	20140120110	20140 1901 100 mard add in	BUILDING INFORMATION MODELING	
19	TAMATAPU TARUN	20531140121	20Nt 1140121 @ oavel adu m	BUILDING INFORMATION MODELING	
20	UPPULURI SATHWIK RAJU	20NEJ140122	20NLI1A0122/@nstit edu in	BUILDING INFORMATION MODELING	
21	VASIREDDI ANUSHA	20NU140123	20NU1A0123@mailt.edu.in	BUILDING INFORMATION MODELING	
22	GUDABA MANOHAR	21NU5A0101	21NU6A0101@positedu in	BUILDING INFORMATION MODELING	
23	KODURU LAXMI PADMAVATHI DEVI	21NU5A0102	21NU5A0102@marit.edu.in	BUILDING INFORMATION MODELING	
24	KOTHURU UPENDRA	21NU5A0103	21NUSA0103@risrit.edu.in	BUILDING INFORMATION MODELING	
25	KUNCHAM V N S S AJAY SRI RAM	21NU5A0104	21NU5A0104@msrit.edu.in	BUILDING INFORMATION MODELING	
26	MUVVALA SIVA	21NU5A0105	21NU5A0105@narit.edu.m	BUILDING INFORMATION MODELING	
27	PONAGANTI DURGA PRASAD	21NU5A0106	21NU5A0106@nard.edu.in	BUILDING INFORMATION MODELING	
28	S.RAKESH	21NU5A0107	21NUSA0107@nsrit.edu.in	BUILDING INFORMATION MODELING	
29	TADUTURI LALITHA	21NU5A0108	21NUSA0106@bnsrit edu in	BUILDING INFORMATION MODELING	
30	THALLAPU REDDY SALKUMAR	21NU5A0109	21NUSA0100@nant edu m	BUILDING INFORMATION MODELING	
31	VADDAOI LOKESH	21NU5A0110	21MJ5A0110@nar5.edu m	BUILDING INFORMATION MODELING	
32	VANKARA ANIL	21NU5A0111	21NU5A0111@narz.edu in	BUILDING INFORMATION MODELING	
33	VENU KONDROTHU	21NU5A0112	21NJSA0112@nsrit edu in	BUILDING INFORMATION MODELING	

DEPT COORDINATOR

N HOD

Head of the Department Dept. of Civil Engg N.S. Roju Institute of Technology (A. Sontyam, Visekhapatham-531175)