



DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING

FACULTY PUBLICATIONS, A.Y. 2016-2017

SL.NO	NAME OF THE AUTHOR	TITLE	ISSN / ISBN NO.	NAME OF PUBLISHER	MONTH & YEAR OF PUBLICATION
1.	Mr.B.Ravi Chandra	FPGA implementation of basic adder circuits using reversible logic gates	2321-2055	IJEEE	June 2016
2.	Mr. R.Kumara Swamy	FPGA implementation of basic adder circuits using reversible logic gates	2321-2055	IJEEE	June 2016
3.	G.Siva Suresh kumar	Performance evolution of DDR3 SDRAM controller for communication Networks	2321-2055	IJEEE	June 2016
4.	Dr.R.P.Das	Investigation of L, t-slots antennas for WiMAX Applications	2321-2152	IJMECE	Sep. 2016
5.	Dr. K Ravi Kumar	Reducing The Grating Lobes And Main Lobe Width For Increasing Range Resolution Using Phase And Frequency Modulated Codes	978-1-4673-9939-5	IEEE Xplore	Nov.2016
6.	Dr.B.Chinna Rao	An Efficient Hybrid Parallel Prefix Adders For Reverse Converters Using QCA Technology	0975-8887	IJCA	Nov. 2016
7.	Dr.R.P.Das	Speech Enhancement Analysis Using PSO And ANFIS Methods	2319-8885	IJSETR	Dec .2016
8.	B. Ravichandra	GSM and Aurduino implementation for anti-theft safe locker	2321-0613	IJSRD	Jan. 2017

9.	Mr. N.Sai Varma	BOT based robo controlling for smart farming	2321-0613	IJSRD	Jan. 2017
10.	Mr Y.Ramana Murthy	Performance evaluation of JAKES model and DENT model for Rayleigh for Fading channels	2321-0613	IJSRD	Jan.2017
11.	Mr. V.G.S. Swaroop	Design and analysis of high speed multiplier with low Threshold leakage current	2349-3585	IJRDT	Feb.2017
12.	Mrs. M.V.S.Roja Ramani	Design And Implementation of Wireless Controlled Surveillance Robot Using IoT & BoT	2321-0613	IJSRD	Mar .2017
13.	Mrs.K.V.E.Sarojini	Wireless sensor based health monitoring system	2348-6848	IJR	Mar .2017
14.	Mr. R.Kumara Swamy	Design and interface of voice module for DEAF and DUMB	2456-3315	IJRTI	Apr .2017
15.	Mrs. B. Sravani	Implementation IT to detect and display underground cable fault	2456-3315	IJRTI	2017