

# E-MINDS



## DEPARTMENT OF EEE

Established in 2008, the Department of EEE has grown steadily, offering programs that cater to the evolving needs of the industry. The department began with a B. Tech program with an initial intake of 60 students in 2008, followed by the launch of an M. Tech program in Power Systems Control and Automation with 24 students in 2014. The department consists of qualified and dedicated faculty who serve as the key pillars supporting the department's growth and success in all aspects. Our curriculum and activities encompass key areas such as power systems, control systems, electrical machines, electromagnetic theory, and computer languages. They also integrate the latest advancements in rapidly growing fields like electric vehicles and renewable energy systems through honour and minor degree, which are essential for modern societal development and industrial progress

### Department Vision

- To attain academic excellence in electrical and electronics engineering by imbibing ethical and moral values and contributing to society through research.

### Department Mission

- M1: To impart technical knowledge through quality education and creating conducive environment through state-of-the-art infrastructure and facilities.
- M2: To promote academia - industry relationship for enhancing multidisciplinary research.
- M3: To imbibe core values of integrity, team work, professional ethics and societal responsibilities

**EDITORIAL MEMBERS**



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Professor & HOD - EEE



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Asst., Professor



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Mr. Challa Sasivardhan Rao,  
IV - EEE



Student Representative  
Ms. G. Ramya,  
III - EEE



Student Representative  
Mr. G. Sasi Preetham,  
II - EEE

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### HOD Message:

It is a privilege and honour to lead the EEE department at this esteemed institution. Our field is at the heart of modern technological advancements, with a particular emphasis on the rapidly growing electric vehicle (EV) sector. This dynamic field encompasses core technologies like power systems, power electronics, electrical machines, control systems, and electromagnetic theory etc., all of which are critical to the development of efficient and sustainable transportation solutions.

To stay at the forefront of innovation, our departmental association, ADVAYA, organizes workshops, technical training, and expert guest lectures. These initiatives focus on emerging trends, including EV technologies, drone, IOT Technologies and aim to equip our students and faculty with the knowledge and skills needed to excel in this exciting domains.

Our department is committed to playing a pivotal role in shaping the future of electric mobility. By fostering research, development, and education in EV-related areas, we strive to create a sustainable and electrified transportation ecosystem.

### Department Profile

Established in 2008, the Department of Electrical and Electronics Engineering has grown steadily, offering programs that cater to the evolving needs of the industry. The department began with a B.Tech program with an initial intake of 60 students in 2008, followed by the launch of an M.Tech program in Power Systems Control and Automation with 24 students in 2014. The department consists of qualified and dedicated faculty who serve as the key pillars supporting the department's growth and success in all aspects. Our curriculum and activities encompass key areas such as power systems, control systems, electrical machines, electromagnetic theory, and computer languages. They also integrate the latest advancements in rapidly growing fields like electric vehicles and renewable energy systems through honour and minor degree, which are essential for modern societal development and industrial progress

The departmental association, ADVAYA, organizes various initiatives like workshops, technical training sessions, guest lectures, and seminars featuring industry experts and academicians. These activities aim to foster continuous knowledge enhancement for both faculty and students, ensuring they stay updated with emerging trends and technologies. The department is committed to developing innovative and skilled graduates who contribute meaningfully to the industry and academia. With the rapid industrial growth in the country, there is a rising demand for electrical engineers in the power sector, private industries, public



sector undertakings (PSUs), and government organizations, creating ample opportunities for our graduates to excel and lead in their careers.

### **Department Vision**

To attain academic excellence in electrical and electronics engineering by imbibing ethical and moral values and contributing to society through research.

### **Department Mission**

**M1:** To impart technical knowledge through quality education and creating conducive environment through state-of-the-art infrastructure and facilities.

**M2:** To promote academia - industry relationship for enhancing multidisciplinary research

**M3:** To imbibe core values of integrity, team work, professional ethics and societal responsibilities

### **Program Educational Objectives**

**PEO 1:** Demonstrate the real-world engineering problem solving skills by applying the fundamental and conceptual engineering knowledge as a practicing Electrical and Electronics Engineer

**PEO 2:** Provide research-based engineering solutions addressing the environment and sustainability by maintaining the professional standards and ethical values.

**PEO 3:** Foster self-directed learning through their professional experience, technology advancements in their relevant field of interest.

**Dr. R. S. R. Krishnam Naidu**  
**Professor & HOD – EEE**

The following are the Activities and Achievements from the department of EEE

S.No	Name of the Activity and Achievement	Organized by	Date of the Event	Page Numbers
1.	Faculty Ph.D. Admission Enrolment	(i) JNTUK (ii) JNTUGV	November	1
2.	Alumni Talk -1 by Mailapalli Raju	Department of EEE, NSRIT	28-09-2024	2
3.	IEEE Day 2024	NSRIT IEEE Student Branch	30-09-2024 to 01-10-2024	3 - 6
4.	Mr.S.Ramanjaneyulu Korada Recognized with Best Mentor Award for Exceptional Mentorship and Student Support	Council for Skill and Competency	05-10-2024	6
5.	Freshers Day 2024	Department of EEE, 2nd year students	18-10-2024	7 - 8
6.	Student Outside participation at TECHRITZ-2K24: A National-Level Tech Fest for Women Engineers	Vignan's Institute of Engineering for Women, located in Kapujaggarajupede, Visakhapatnam	25-10-2024 to 26-10-2024	9
7.	Alumni Talk – 2 by Mr. Himanshu Reddipalli	Department of EEE, NSRIT	26-10-2024	9 - 10
8.	Alumni Talk – 3 by Mr.Kenuguva Uma Maheswar	Department of EEE, NSRIT	30-10-2024	10 - 11
9.	Two-days workshop on Outcome Based Workshop Education (OBE)	Department of Computer Science Engineering, NSRIT	October	11
10.	NPTEL Online Certifications	NPTEL	July to Oct	12 - 13

### 1. Faculty Ph.D. Admission Enrolment

Two of our EEE faculty members have been enrolled into the part-time **Ph.D. program**

(i). **Mr. K.M.M. Tarakesh**, who secured the **66th rank**, has been provisionally selected for the Ph.D. program (part-time) and admitted to the Department of Electrical and Electronics Engineering, JNTUK, Kakinada

(ii). **Mrs. S. Yamini**, who secured the **143rd rank**, has also been provisionally selected for the part-time Ph.D. program and admitted to the Department of Electrical and Electronics Engineering, JNTUGV, Vizianagaram.

**APRCET 2023-24**  
Conducted by Sri Venkateswara University, Tirupati  
on behalf of APSCHE  
Provisional Allotment Letter of Second & Final Phase

Hall Ticket Number:	9550947038
Candidate Name:	KOLISU MOHAN MURALI TARAKESH
Gender:	MALE
Caste / Region:	BC, B / AU
Rank:	66
Allotted (PT/PT):	Part Time
Father's Name:	KOLISU SRINIVASA RAO

**This is to inform that the options exercised by the candidate have been processed based on merit, rank, local area, gender, Caste, category, Special reservation category (CAPPHNCCSPORTS) etc., and the candidate has been allotted a seat in 2nd & Final Phase at**

Name of the University: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY (JNTUK) (UPE)  
Allotted College/Research Center: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, KAKINADA CAMPUS (KAC) (UPE)  
P.T.D Programme in the Department: ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING  
Allotted Seat Category: BC, B, C, UR

Name of the University: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY (JNTUGV) (UPE)  
Allotted College/Research Center: JNTUGV, VIZIANAGARAM CAMPUS (VCA) (UPE)  
P.T.D Programme in the Department: ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING  
Allotted Seat Category: BC, B, C, UR

**Instructions to the Candidates:**

- The Candidate is instructed to download the allotment letter from the website <http://rsets.apsche.ap.gov.in>. Click on the self-reporting button to complete the self-reporting by the candidate and download the joining report.
- Report to the allotted University along with all original certificates and two copies of joining report and allotment letter.
- Submit a copy of allotment letter and joining report at the allotted university. Obtain acknowledgement on second copy and retain the same with you.
- Both Self-reporting and reporting at the allotted University are compulsory to retain the present allotment. The last date for self-reporting and the reporting at the allotted University is 16-11-2024.
- If you do not report through self-reporting system and not reporting at the allotted University, the provisional allotment will be cancelled and you will not have claim on the seat allotted.
- Seat obtained candidate in Affiliated colleges/Research centers of the University has to report at the University before reporting at the affiliated college/Research centers.
- At the time of reporting, Candidate who does not submit original Transfer Certificate and PG provisional / OD higher seat will be cancelled.
- For Part time candidates service certificates and NOC are a must and if not produced seat will be cancelled.
- Residence certificate should be submitted for who studied in Distance mode/Telangana State for their period of study.
- Pay the fee in the allotted University as per the regulations of the concerned University.
- The academic credentials verified if found false at a later date, the allotment will be cancelled and will also be liable for criminal prosecution.
- The Candidates who sit in the 2nd and Final phase from the 1st phase admitted seat can collect their original certificates from the 1st phase admitted universities and submit the same in the 2nd & Final phase allotted universities.

**INSTRUCTIONS TO THE UNIVERSITIES**

All the Universities/Centers are requested to verify the original certificates of the allotted candidates thoroughly and send the notice of the Governor, AP/CE-1/022-24 Admissions for any verification.

The allotted candidates of 1st phase may be retained at original certificates and submit the fee and other duty verifying the 2nd & Final phase allotment letter.

ORIGINAL CERTIFICATES TO BE SUBMITTED AT 1st & 2nd PHASE OF ADMISSIONS IN ALLIATED UNIVERSITIES

**Original Certificates:**

- Transfer Certificate (TC)
- 10th Certificate and Mark Sheet
- 12th Certificate and Mark Sheet (SC/ST/BC/AB/UR) and (DSE Category)
- Service Certificate and NOC (For Part-time Candidates)
- Residence Certificate and PG Provisional / OD (For Part-time Candidates)
- Study Certificate (Intermediate to PG) Residence certificate in the prescribed format of support of local state as defined in G.O.P No. 466 dt. 10.07.2019 and its amendments.
- Copy of the Allotment letter and Self-reporting form (2 Copies).

\*\*\* This computer generated Provisional Allotment Order does not require any authentication. \*\*\* 16/11/2024 11:24 AM

**APRCET 2023-24**  
Conducted by Sri Venkateswara University, Tirupati  
on behalf of APSCHE  
Provisional Allotment Letter of Second & Final Phase

Hall Ticket Number:	9550947012
Candidate Name:	SINGAMPALLI YAMINI
Gender:	FEMALE
Caste / Region:	BC, D / AU
Rank:	143
Allotted (PT/PT):	Part Time
Father's Name:	SINGAMPALLI SANYASI RAO

**This is to inform that the options exercised by the candidate have been processed based on merit, rank, local area, gender, Caste, category, Special reservation category (CAPPHNCCSPORTS) etc., and the candidate has been allotted a seat in 2nd & Final Phase at**

Name of the University: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY (JNTUGV) (UPE)  
Allotted College/Research Center: JNTUGV, VIZIANAGARAM CAMPUS (VCA) (UPE)  
P.T.D Programme in the Department: ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING  
Allotted Seat Category: BC, D, UR

Name of the University: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY (JNTUK) (UPE)  
Allotted College/Research Center: JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, KAKINADA CAMPUS (KAC) (UPE)  
P.T.D Programme in the Department: ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING  
Allotted Seat Category: BC, B, C, UR

**Instructions to the Candidates:**

- The Candidate is instructed to download the allotment letter from the website <http://rsets.apsche.ap.gov.in>. Click on the self-reporting button to complete the self-reporting by the candidate and download the joining report.
- Report to the allotted University along with all original certificates and two copies of joining report and allotment letter.
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- Residence certificate should be submitted for who studied in Distance mode/Telangana State for their period of study.
- Pay the fee in the allotted University as per the regulations of the concerned University.
- The academic credentials verified if found false at a later date, the allotment will be cancelled and will also be liable for criminal prosecution.
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ORIGINAL CERTIFICATES TO BE SUBMITTED AT 1st & 2nd PHASE OF ADMISSIONS IN ALLIATED UNIVERSITIES

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- Residence Certificate and PG Provisional / OD (For Part-time Candidates)
- Study Certificate (Intermediate to PG) Residence certificate in the prescribed format of support of local state as defined in G.O.P No. 466 dt. 10.07.2019 and its amendments.
- Copy of the Allotment letter and Self-reporting form (2 Copies).

\*\*\* This computer generated Provisional Allotment Order does not require any authentication. \*\*\* 16/11/2024 11:24 AM

**AP RCET 2023 - 24 ADMISSIONS**  
Research Common Entrance Test  
(ADMISSIONS INTO P.D PROGRAMMES)

**JOINING DETAILS**

Hall Ticket No:	9550947038	Rank:	66
Name:	KOLISU MOHAN MURALI TARAKESH	Father's Name:	KOLISU SRINIVASA RAO
Gender:	MALE	Caste:	BC, B
Allotted Institute:	JNTUK_CAMPUS_01	Allotted Branch:	ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING

Based on your acceptance to join JNTUK\_CAMPUS\_01 ELECTRICAL AND ELECTRONICS ENGINEERING-ELECTRICAL AND ELECTRONICS ENGINEERING through self reporting system on date: 12/11/2024

Your joining details are confirmed vide Hallticket Number: 9550947038

Note: Submit this along with provisional allotment order already downloaded

**CONVENOR**  
AP RCET-2023-24 ADMISSIONS

ds@jntugv.edu.in

**DIRECTORATE OF ADMISSIONS**  
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, GURAJADA-VIZIANAGARAM  
Vizianagaram-535 003 Andhra Pradesh(India)  
(Established by Andhra Pradesh Act No.22 of 2021)

Sl.No:156 Date: 15-11-2024

**ADMISSION LETTER**

Mr./Ms./Sri SINGAMPALLI YAMINI is provisionally selected for Ph.D Programme (Part-Time) and admitted in the Department of Electrical & Electronics Engineering, JNTUGV, Vizianagaram.

The details of Mr./Ms./Sri./Smt. SINGAMPALLI YAMINI, as per the Provisional Allotment Letter issued by the Convener, APRCET-2023-24 Admissions is mentioned as:

Father's Name	Mr. SINGAMPALLI SANYASI RAO
Gender/Caste/Region	Female/BC-D/AU
APRCET-2023 Hall Ticket Number	95509417012
APRCET-2023 Rank	143

**15/11/24**  
**15 11 24**  
**DIRECTOR**  
Director, Admissions  
JNTUGV  
Vizianagaram-535003

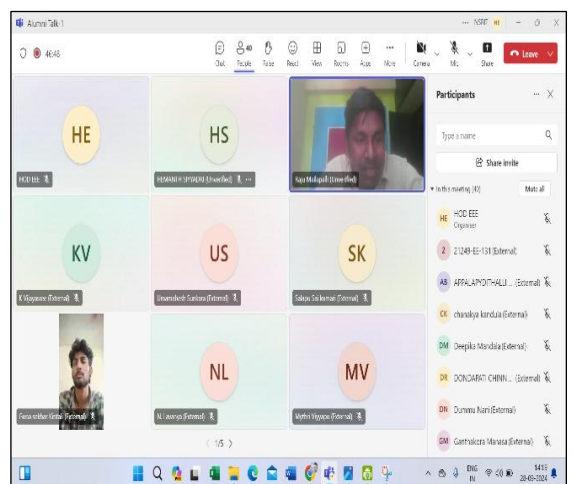
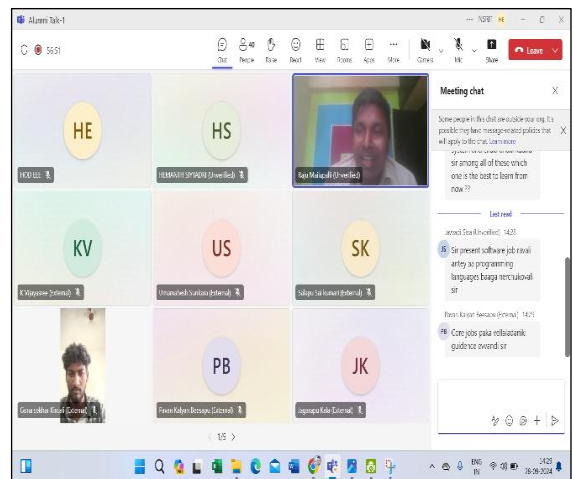
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## 2. Alumni Talk – 1 by Mailapalli Raju

(i). On 28th September 2024, Mr. Mailapalli Raju, Pega Developer at TCS, shared insights on transitioning from EEE to IT.

(ii). The session highlighted continuous learning, adaptability, and programming skills for career growth, concluding with a Q&A session.

The Department of Electrical and Electronics Engineering (EEE) organized alumni talk on Saturday, 28th September 2024. The speaker, Mr. Mailapalli Raju, an alumnus currently working as a Pega Developer at Tata Consultancy Services (TCS), Hyderabad, shared his career journey. He discussed how his EEE background helped him transition into the IT industry, specifically in Pega Systems. Mr. Raju highlighted the importance of continuous learning, adaptability, and gaining new skills, like programming, for career growth. The session concluded with a Q&A, offering valuable career advice to students.



### 3. IEEE Day 2024

**Resource Person:** Mr. MID. Khwaja Mulnuddin Chisti

**Venue:** Mechanical Seminar Hall

**Date:** 30.09.2024 to 01.10.2024

**DAY-1:** The event commenced with the official inauguration of IEEE Day, celebrating the significant role of IEEE in advancing technology for humanity. The event was graced by dignitaries from the college administration, faculty members and students. The Head of Department (HOD) of Electrical Engineering Dr.RSR Krishnam Naidu Sir delivered an insightful talk on the importance of industry-academia collaboration and how IEEE initiatives help in shaping future engineers by providing them with exposure to the latest trends in technology. Professor Shambu Prasad sir, Principal addressed the gathering, highlighting the need for interdisciplinary collaboration and the role of IEEE in fostering a platform for innovation and research. The Principal emphasized the importance of active participation in technical events and competitions for holistic learning.

The HODs of the ECE (Electronics and Communication Engineering) Dr.B.Siva Prasad sir, CSE (Computer Science Engineering), Dr.Rayudu Srinnivas sir and CSD(Computer systems and Design) Dr.K.Madhavi mam, departments shared their perspectives on technological advancements in their respective fields. They encouraged students to actively engage in IEEE activities and continue their journey of learning beyond the classroom.



**IEEE DAY 2024**  
 Organised by  
**NSRIT IEEE STUDENT BRANCH**  
 30.09.2024 to 01.10.2024

For Every Event  
 Prizes :  
 1st Prize - 500/-  
 2nd Prize - 300/-

**Technical Quiz**  
 (100/- Per Team 5 Members)

**PPT**  
 ("Leveraging Technology for a better Tomorrow")  
 (100/- Per Team 2 Members)

**Poster Presentation**  
 ("Leveraging Technology for a better Tomorrow")  
 (100/- Per Team 2 Members)

**Guest Speech & Prize Distribution**

**Speaker**  
  
 MD Khosja Mahmudul Chasi  
 Treasurer - IEEE Vijay Bay Section for 2023-24  
 Assistant Professor  
 Dept. Of Electrical, Electronics, and Communication Engineering (EECE)  
 GITAM Deemed to be University  
 Contact If any Queries

**CONTACT IF ANY QUERIES**

Dr.R.S.RKrishnan Naidu Counselor NSRIT IEEE SB 987491338	V.Divakar Chair NSRIT IEEE SB 938775013	Ch.Bhavya Vice-Chair NSRIT IEEE SB 639844448	N.Sujatha Secretary NSRIT IEEE SB 813732448	B.Sathvika Treasurer NSRIT IEEE SB 939267796	P.P. Web M 8
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**DAY-2:**

Day 2 began with the competition for poster presentations. This competition's primary goal is to assess students' knowledge of numerous technical subjects and recent developments in engineering. Five batches of students from several branches, including ECE, CSM, CSE, and EEE, are competing. Second-year EEE students took first place, while third-year ECE students took second.

The resource person Mr.MID.Khwaja Mulnuddin Chisti has addressed the students and delivered the insights regarding the image contrast, image restoration, spatial resolution related to images and artificial intelligence.



The quiz and poster presentation competition on IEEE day was a successful event that fostered knowledge, creativity and engagement among students. The prize distribution ceremony celebrated the hard work of participants and reinforced the spirit of innovation and collaboration within the IEEE community.

The guest facilitation during IEEE day was a significant success, providing valuable insights and inspiration to all participants. The event not only celebrated the achievements of IEEE but also highlighted the importance of collaboration and continuous learning in the field of technology. We are grateful to our esteemed guests for their contributions and look forward to future collaborations.

Esteemed experts from various fields delivered thought-provoking talks that inspired the participants to explore new horizons in their careers and engage in transformative projects. The sessions were filled with rich discussions, fostering a sense of community among students, professionals, and academics alike.

We extend our heartfelt gratitude to our distinguished guests for their insightful contributions. Their expertise and experiences have left a lasting impact on everyone involved. We eagerly anticipate future collaborations that will continue to promote growth, innovation, and excellence within the IEEE community and beyond.



#### 4. Mr. S. Ramanjaneyulu Korada Recognized with Best Mentor Award for Exceptional Mentorship and Student Support

Mr. S. Ramanjaneyulu Korada has been honored with the prestigious Best Mentor Award for his exceptional commitment to student success on 5<sup>th</sup> October, 2024. His unwavering support, guidance, and dedication have significantly impacted the lives of many students, helping them achieve their academic and professional goals. Through his mentorship, he has fostered a nurturing environment that encourages growth and learning. This well-deserved recognition is presented by the Council for Skills and Competencies (CSC), acknowledging his remarkable contributions to shaping the future of students. Mr. Korada's mentorship continues to inspire and make a lasting difference, leaving a legacy of excellence in education



Department of Electrical and Electronics Engineering – NSRIT(A)

## 5.Freshers Day 2024

**Chief Guests:** Principal - Dr. S. Sambhu Prasad Sir, CMO - Dr. Subba Raju Sir,  
HOD – Dr. R. S. R. Krishnam Naidu Sir

**Venue:** ECE Seminar Hall

**Date:** 18<sup>th</sup> October 2024



Photo Gallery of Freshers day Event



**Dance Performances by the Students**

The **Freshers' Day** event was organized by the 2nd-year students to welcome their juniors in a warm and vibrant manner. The event began with a brief interaction session, where the senior students shared their experiences and offered valuable advice to the freshers. This was followed by exciting dance performances, showcasing the talent of both 2nd-year and fresher students. The event also featured speeches by faculty members and senior students, who encouraged the newcomers to make the most of their time at university. The day ended on a sweet note with a cake-cutting ceremony, symbolizing the beginning of a new chapter for the freshers.



**Cake Cutting**

**Department of Electrical and Electronics Engineering – NSRIT(A)**

## 6. Student Outside participation at TECHRITZ-2K24: A National-Level Tech Fest for Women Engineers

(i). Students P. Pavani, Vijaya Sree, Sai Krishna Lahari, and R. Deepthi Catharine participated in the Poster Presentation at TECHRITZ-2K24, a national-level tech fest for women engineers, held on October 25th–26th, 2024.

(ii). The event, hosted at Vignan's Institute of Engineering for Women, Visakhapatnam, highlighted their commitment to technological innovation and empowering women engineers

Our students P. Pavani, Vijaya Sree, Sai Krishna Lahari, R. Deepthi Catharine participated in the Poster Presentation at TECHRITZ-2K24, a two-day national-level tech fest for women engineers. The event took place on October 25th–26th, 2024, at Vignan's Institute of Engineering for Women, located in Kapujaggarajupedu, Visakhapatnam, Andhra Pradesh. Their involvement in this prestigious event highlights their dedication to technological innovation and their active participation in empowering women engineers in the field.



## 7. Alumni Talk – 2 by Mr. Himanshu Reddipalli

(i). Mr. K.M.M. Tarakesh coordinated an Online Alumni Meet for 2nd and 3rd-year EEE students, featuring Mr. Himanshu Reddipalli as the speaker on 26<sup>th</sup> Oct, 2024.

(ii). Mr. Reddipalli shared valuable career insights, and the event strengthened alumni-student connections, fostering knowledge sharing and career advice.

An Online Alumni Meet was successfully coordinated by Mr. K.M.M. Tarakesh for the 2nd and 3rd-year EEE students. The event featured Mr. Himanshu Reddipalli as the speaker of the day. During the meet, Mr. Reddipalli shared valuable insights from his professional journey, offering guidance and inspiration to the students. This initiative aimed to strengthen the connection between alumni and current students, providing a platform for knowledge sharing and career advice. Mr. Tarakesh's coordination ensured the smooth conduct of the event, fostering a productive and engaging environment for all participants.





**IEEE**  
STUDENT BRANCH NSRIT

**NSRIT**  
AUTONOMOUS

**TALK ON CAREER OPPURTUNITIES IN EEE**  
Organized By Dept. of EEE  
**A.Y. 2024-2025**

**WEDNESDAY**  
30 October 2024

**TIME**  
01.00 pm-04.00 pm

**SPEAKER**  
**KENGUVA UMA MAHESWAR**  
2020-2024 Batch Alumni  
Tata Consultancy Services  
7 LPA Package

**INSTITUTION'S INNOVATION COUNCIL**  
(Ministry of Education Initiative)

### 9. Two-days workshop on Outcome Based Workshop Education (OBE)

NSRIT hosted a 2-day workshop on Outcome-Based Education and Accreditation, featuring expert sessions by Dr. S. Viswanadha Reddy, focusing on teaching methods, curriculum design, and assessment strategies.

- (i). NSRIT hosted a 2-day OBE and OBA workshop with Dr. Reddy.
- (ii). Faculty discussed effective teaching, curriculum design, and assessment strategies.

This 2-day workshop is on Outcome-Based Education (OBE) and Outcome-Based Accreditation (OBA) was conducted at NSRIT College. The workshop aimed to provide valuable insights into the principles and practices of OBE and OBA, which are essential for enhancing the quality of education and ensuring academic excellence. Dr. S. Viswanadha Reddy, a distinguished speaker from JNTUH, Jagityala, delivered expert sessions during the event. The workshop took place in the CSE seminar hall, bringing together faculty members and educators to discuss effective teaching methods, curriculum design, and assessment strategies aligned with outcome-based approaches.



**10. NPTEL Online Certifications**

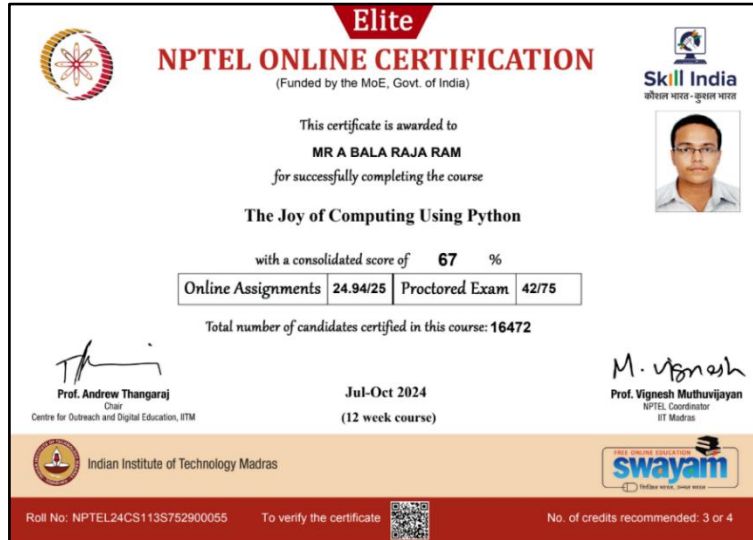
S.NO	NAME	DESIGNATION	NAME OF THE NPTEL COURSE	SCORE
1.	Mr.K.S.Ramanjaneyulu	Assistant Professor	Cloud Computing	76%
2.	Mr.A.Bala Raja Ram	Assistant Professor	The joy of computing Python	67%
3.	Mr.K.S.Ramanjaneyulu	Assistant Professor	Cyber Security and Privacy	55%

(i). **Mr. S. Ramanjaneyulu Korada** has successfully completed the NPTEL online certification course in **Cloud Computing**, achieving a consolidated score of **76%**. The 12-week course, attended by 30,816 candidates, provided in-depth knowledge of cloud computing technologies and their applications. This accomplishment reflects the dedication to professional growth and his commitment to staying updated with the latest trends in the rapidly evolving tech industry. By completing this challenging course has demonstrated his expertise and skills in cloud computing, further enhancing his ability to contribute to the field of technology and innovation.



(ii) **Mr. A. Bala Raja Ram** has successfully completed the NPTEL online certification course in "**The Art of Computing Using Python**," achieving a consolidated score of **67%**. The 12-week course, attended by 16,472 candidates, provided comprehensive insights into Python programming and its applications in computing. This

accomplishment reflects the commitment to enhancing his programming skills and staying updated with current technologies. By completing this the course has further strengthened his expertise in Python, which is essential for solving complex computational problems and contributing to innovative solutions in the field of technology.



(iii). **Mr. S. Ramanjaneyulu Korada** has successfully completed the NPTEL online certification course in **Cyber Security and Privacy**, achieving a consolidated score of **55%**. The 12-week course, attended by 6,461 candidates, covered essential concepts in cyber security, privacy protection, and best practices for safeguarding digital information. This accomplishment demonstrates the commitment to understanding and applying critical security measures in today's digital world. By completing this course, he has enhanced his knowledge in cyber security, equipping him with the skills needed to address emerging security challenges and contribute to building safer digital environments.

